



Supply Voltage Supervisor with Open-Drain Reset

1 FEATURES

- Operating Voltage Range:1.2V to 5.5V
- Low Power Consumption:50µA (Max)
- Precision Supply-Voltage Monitor: 2.63V, 2.93V, 3.08V, 4.00V, 4.65V
- Guaranteed RESET Valid at V_{CC}=1.2V
- 200ms Reset Pulse Width
- Voltage Monitor for Power-Fail or Low-Battery Warning
- Operating Temperature Range: -40°C to +125°C
- Open-Drain, RESET Output
- Available in Green Package: SOT23

2 APPLICATIONS

- Computers
- SOC \ DSP or Micro controllers
- Embedded Systems
- Industrial Equipment
- Intelligent Instruments
- Critical µP Power Monitoring
- Wireless Communications Systems

3 DESCRIPTIONS

The RS803 microprocessor (μP) supervisory circuits reduce the complexity and number of components required to monitor power-supply and battery function in μP systems. This device significantly improves system reliability and accuracy compared to separate ICs or discrete components.

These circuits perform a single function: they assert a reset signal whenever the $V_{\rm CC}$ supply voltage declines below a preset threshold, keeping it asserted for at least 200ms after $V_{\rm CC}$ has risen above the reset threshold. Reset thresholds suitable for operation with a variety of supply voltages are available.

The RS803 has an open-drain output stage. The RS803's open-drain \overline{RESET} output requires a pull-up resistor that can be connected to a voltage higher than Vcc. The RS803 have an active-low \overline{RESET} output. The reset comparator is designed to ignore fast transients on Vcc, and the outputs are guaranteed to be in the correct logic state for Vcc down to 1.2V.

Low supply current makes the RS803 ideal for use in portable equipment. The RS803 is available in Green SOT23 package. It operates over an ambient temperature range of -40°C to +125°C.

Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS803	SOT23(3)	1.30mm×2.92mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

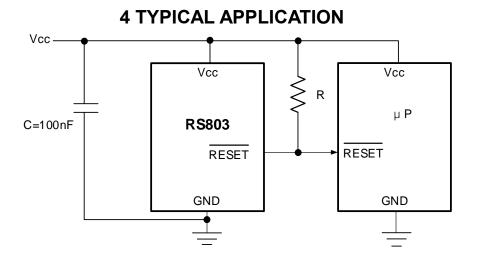




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5 Revision HistoryNote: Page numbers for previous revisions may different from page numbers in the current version.

Version	Change Date	Change Item
A.1	2021/08/09	Initial version completed
A.2	2023/07/24	1. Update PACKAGE MARKING on Page 5@RevA.1 2. Update Operating Temperature Range: -40°C to +125°C 3. Modify Operating Voltage Range: 1.2V to 5.5V 4. Update ESD Ratings 5. Added RS803-4.65YSF3 ORDERING NUMBER



6 PACKAGE/ORDERING INFORMATION (1)

PRODUCT ORDERING NUMBER				PACKAGE MARKING (2/3)	PACKAGE OPTION	
	RS803-2.63YSF3	-40°C ~+125°C	SOT23	803B	Tape and Reel,3000	
	RS803-2.93YSF3	-40°C ~+125°C	SOT23	803C	Tape and Reel,3000	
RS803	RS803-3.08YSF3	-40°C ~+125°C	SOT23	803D	Tape and Reel,3000	
	RS803-4.00YSF3	-40°C ~+125°C	SOT23	803E	Tape and Reel,3000	
	RS803-4.65YSF3	-40°C ~+125°C	SOT23	803G	Tape and Reel,3000	

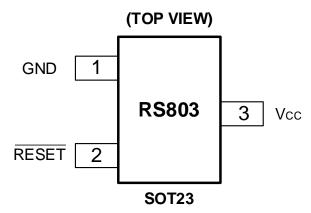
NOTE:

⁽¹⁾ This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.

⁽²⁾ There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.
(3) B, C, D, E, G represents different Reset Thresholds.



7 PIN CONFIGURATIONS



PIN DESCRIPTION

PIN	NAME	FUNCTION			
SOT23	INAIVIE	FUNCTION			
1	GND	Ground, reference for all signals.			
2	RESET	Active-Low Reset Output remains low while V_{CC} is below the reset threshold, and for at least 200ms after V_{CC} rises above the reset threshold.			
3	Vcc	Power Supply Voltage that is monitored.			



8 Specifications

8.1 Absolute Maximum Ratings (1)

over operating free-air temperature range (unless otherwise noted) (1)(2)

			MIN	MAX	UNIT
Vcc	Supply voltage range		-0.5	6.0	V
Vı	Input voltage range (2)		-0.5	6.0	V
Vo	Voltage range applied to any output in the high-impedance or power-off state (2)		-0.5	6.0	V
Vo	Voltage range applied to any output in the high or low sta	age range applied to any output in the high or low state (2)(3)		Vcc+0.5	V
lıĸ	Input clamp current V	/ı<0		-20	mΑ
lok	Output clamp current V	/ ₀ <0		-20	mA
Ιο	Continuous output current			±20	mA
	Continuous current through Vcc or GND			±20	mA
θја	Package thermal impedance (4)	SOT23		295	°C/W
TJ	Junction temperature (5)		-65	150	°C
T _{stg}	Storage temperature		-65	150	°C
TA	Operating temperature		-40	125	°C

⁽¹⁾ Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of V_{CC} is provided in the Recommended Operating Conditions table.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of T_{J(MAX)}, R_{θJA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} T_A) / R_{θJA}. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
V	Electrostatic discharge	Human-body model (HBM), MIL-STD-883K METHOD 3015.9	±4000	V
V _(ESD)	Electiostatic discharge	Machine model (MM), JESD22-A115C (2010)	±200	V



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.



8.3 ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 2.74 \text{V to } 5.5 \text{V for RS803-} 2.63; V_{CC} = 3.05 \text{V to } 5.5 \text{V for RS803-} 2.93; V_{CC} = 3.21 \text{V to } 5.5 \text{V for RS803-} 3.08; V_{CC} = 4.17 \text{V to } 5.5 \text{V for RS803-} 4.00; V_{CC} = 4.84 \text{V to } 5.5 \text{V for RS803-} 4.65; T_A = -40 ^{\circ}\text{C to } +125 ^{\circ}\text{C}, \text{ unless otherwise noted, typical at } 25 ^{\circ}\text{C}.)$

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Operating Voltage Range	Vcc		1.2		5.5	V	
Supply Current	ISUPPLY			20	50	μA	
		RS803-2.63	2.50	2.63	2.74		
Reset Threshold		RS803-2.93	2.80	2.93	3.05		
	V_{RT}	RS803-3.08	2.94	3.08	3.21	V	
		RS803-4.00	3.82	4.00	4.17		
		RS803-4.65	4.44	4.65	4.84		
		RS803-2.63		12			
		RS803-2.93		14		1	
Reset Threshold Hysteresis		RS803-3.08		15		mV	
		RS803-4.00		20			
		RS803-4.65		23			
Reset Pulse Width	t _{RS}		100	200	460	ms	
Reset Threshold Temperature Coefficient (1)				30		ppm/°C	
V _{CC} to RESET delay	t _{RD}	V _{CC} =3.3V, RS803-2.93		33		μs	
RESET Output voltage	Low	I _{OL} = 1.2mA			0.4	V	
RESET Open-Drain output Leakage Current	I _{Ikg(OD)}	V _{CC} > V _{RT} , RESET deasserted			1	μA	

⁽¹⁾ This parameter is ensured by design and/or characterization and is not tested in production.



8.4 TYPICAL OPERATING CHARACTERISTICS

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

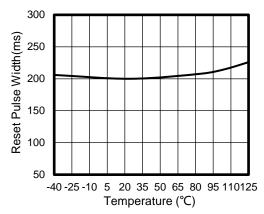


Figure 1. Reset Pulse Width vs Temperature

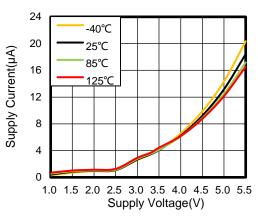


Figure 2. Supply Voltage vs Supply Current

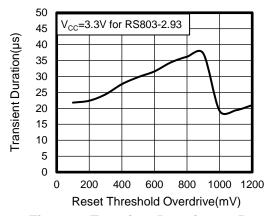


Figure 3. Transient Duration vs Reset
Threshold Overdrive

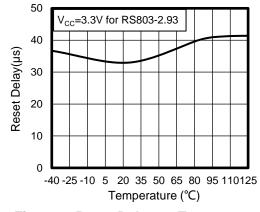


Figure 4. Reset Delay vs Temperature

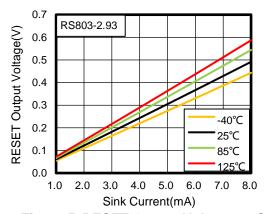


Figure 5. RESET Output Voltage vs Sink
Current

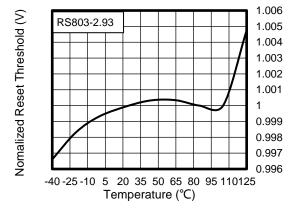
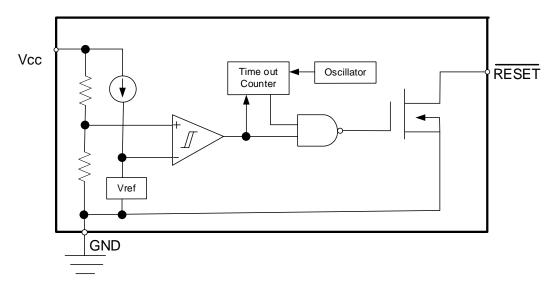


Figure 6. Normalized Reset Threshold vs
Temperature



9 Function Block Diagram



10 Detailed Description

A microprocessor's (μ P's) reset input starts the μ P in a known state. The RS803 assert reset to prevent code-execution errors during power-up, power-down or brownout conditions. They assert a reset signal whenever the V_{CC} supply voltage declines below a preset threshold, keeping it asserted for at least 200ms after V_{CC} has risen above the reset threshold. The RS803 uses an open-drain output. Connect a pull-up resistor on the RS803's RESET output pin to any supply voltage between 0V to 6V.



11 Applications Information

11.1 Interfacing to µPs with Bidirectional Reset Pins

Since the \overline{RESET} output on the RS803 is open drain, this device interfaces easily with μPs with bidirectional reset pins, Connecting the μP supervisor's \overline{RESET} output directly to the μP 's \overline{RESET} pin with a single pull-up resistor allows either device to assert reset (Figure 7).

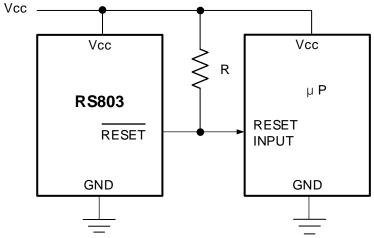


Figure 7. Interfacing to µP's with Bidirectional Reset I/O

11.2 RS803 Open-Drain RESET Output Allows Use with Multiple Supplies

The pull-up connected to the RS803 will connect to the supply voltage that is being monitored at the μ P's V_{CC} pin. However, some systems may use the open-drain output to level-shift from the monitored supply to reset circuitry powered by some other supply (Figure 8). Note that as the RS803's V_{CC} decreases below 1.2V, so does the μ P's ability to sink current at \overline{RESET} . Also, with any pull-up, \overline{RESET} will be pulled high as V_{CC} decays toward 0V. The voltage where this occurs depends on the pull-up resistor value and the voltage to which it is connected.

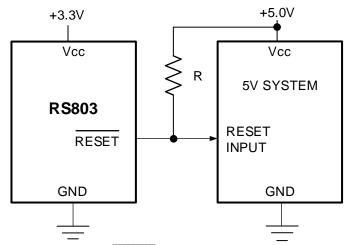
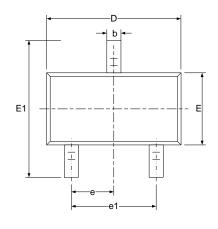
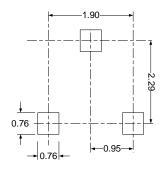


Figure 8. RS803 Open-Drain RESET output Allows Use with Multiple Supplies

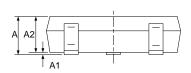


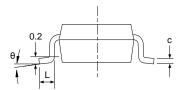
12 PACKAGE OUTLINE DIMENSIONS SOT23 (3)





RECOMMENDED LAND PATTERN (Unit: mm)





Cymphal	Dimensions I	n Millimeters	Dimensions In Inches			
Symbol	Min	Max	Min	Max		
A ⁽¹⁾	0.900	1.150	0.035	0.045		
A1	0.000	0.100	0.000	0.004		
A2	0.900	1.050	0.035	0.041		
b	0.300 0.500 0.012		0.012	0.020		
С	0.080	0.150	0.003	0.006		
D ⁽¹⁾	2.800	3.000	0.110	0.118		
E (1)	1.200	1.400	0.047	0.055		
E1	2.250	2.550	0.089	0.100		
е	e 0.950 (BSC)		0.037 (BSC) (2)		
e1	1.800	2.000	0.071	0.079		
L	0.300	0.500	0.012	0.020		
θ	0°	8°	0°	8°		

NOTE:

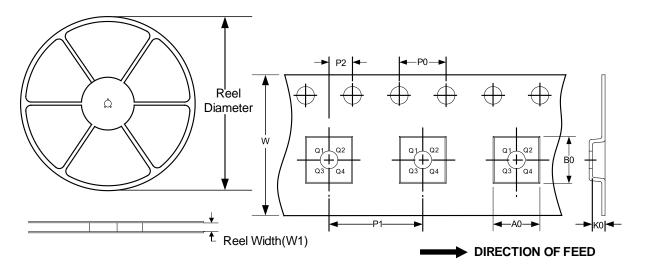
- Plastic or metal protrusions of 0.15mm maximum per side are not included.
 BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
 This drawing is subject to change without notice.



13 TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOT23	7"	9.5	3.15	2.77	1.22	4.0	4.0	2.0	8.0	Q3

NOTE:

^{1.} All dimensions are nominal.

^{2.} Plastic or metal protrusions of 0.15mm maximum per side are not included.



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