

# General-Purpose High-Voltage Open-Drain Output Dual Comparator

## 1 FEATURES

- **Supply Range: +3V to +32V**
- **Low Supply Current**  
**20 $\mu$ A (TYP) per channel at  $V_S = 5V$**
- **Common-Mode Input Voltage Range**  
**Includes Ground**
- **Low Output Saturation Voltage**
- **Open-Drain Output for Maximum Flexibility**
- **SPECIFIED UP TO +125°C**
- **Micro SIZE PACKAGES: SOP8**

## 2 APPLICATIONS

- **Hysteresis Comparators**
- **Factory Automation & Control**
- **Industrial Equipment**
- **Test and Measurement**
- **Cordless Power Tool**
- **Vacuum Robot**
- **Wireless Infrastructure**

## 3 DESCRIPTIONS

The LM393 is the dual comparator version, and the outputs can be connected to other open-collector outputs to achieve wired-AND relationships. It can operate from 3V to 32V, and have low power consuming 20 $\mu$ A (TYP) per channel.

The LM393 consist of two independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Quiescent current is independent of the supply voltage. The device is the most cost-effective solutions for applications where low offset voltage, high supply voltage capability, low supply current, and space saving are the primary specifications in circuit design for portable consumer products.

The LM393 is available in Green SOP8 packages. It operates over an ambient temperature range of -40°C to +125°C.

**Device Information (1)**

PART NUMBER	PACKAGE	BODY SIZE (NOM)
LM393	SOP8	4.90mmx3.90mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

## Table of Contents

<b>1 FEATURES</b> .....	1
<b>2 APPLICATIONS</b> .....	1
<b>3 DESCRIPTIONS</b> .....	1
<b>4 Revision History</b> .....	3
<b>5 PACKAGE/ORDERING INFORMATION</b> <sup>(1)</sup> .....	4
<b>6 Pin Configuration and Functions (Top View)</b> .....	5
<b>7 SPECIFICATIONS</b> .....	6
7.1 Absolute Maximum Ratings .....	6
7.2 ESD Ratings .....	6
7.3 Recommended Operating Conditions .....	6
7.4 Thermal Information: LM393 .....	7
7.5 ELECTRICAL CHARACTERISTICS .....	8
7.6 TYPICAL CHARACTERISTICS .....	9
<b>8 Detailed Description</b> .....	10
8.1 Overview .....	10
<b>9 Application and Implementation</b> .....	11
9.1 Application Information .....	11
9.2 Typical Application .....	11
9.3 Detailed Design Procedure .....	11
9.4 Input Voltage Range .....	11
<b>10 Layout</b> .....	12
10.1 Layout Guidelines .....	12
10.2 Layout Example .....	12
<b>11 PACKAGE OUTLINE DIMENSIONS</b> .....	13
<b>12 TAPE AND REEL INFORMATION</b> .....	14

## 4 Revision History

Note: Page numbers for previous revisions may differ from page numbers in the current version.

VERSION	Change Date	Change Item
A.0	2020/10/11	Preview version completed
A.1	2021/02/14	Initial version completed
A.2	2022/07/16	1. Add TAPE AND REEL INFORMATION 2. Update the ESD Ratings 3. Change Supply Range: +3V to +32V
A.2.1	2024/02/23	Modify packaging naming

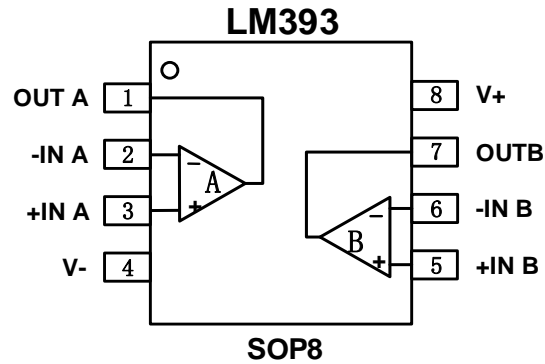
**5 PACKAGE/ORDERING INFORMATION <sup>(1)</sup>**

Orderable Device	Package Type	Pin	Channel	Op Temp(°C)	Device Marking <sup>(2)</sup>	Package Qty
LM393XK	SOP8	8	2	-40°C ~+125°C	LM393	Tape and Reel,4000

**NOTE:**

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.

## 6 Pin Configuration and Functions (Top View)



### Pin Description

NAME	PIN	I/O <sup>(1)</sup>	DESCRIPTION
	SOP8		
OUTA	1	O	Output, channel A
-INA	2	I	Inverting input, channel A
+INA	3	I	Noninverting input, channel A
V-	4	P	Negative (lowest) power supply
+INB	5	I	Noninverting input, channel B
-INB	6	I	Inverting input, channel B
OUTB	7	O	Output, channel B
V+	8	P	Positive (highest) power supply

(1)I=Input, O=Output, P=Power

## 7 SPECIFICATIONS

### 7.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

		MIN	MAX	UNIT
Voltage	Supply, $V_S=(V+) - (V-)$		36	V
	Input pin (IN+, IN-) <sup>(2)</sup>	(V-)-0.3	(V+) +0.3	
	Signal output pin <sup>(3)</sup>	(V-)-0.3	(V+) +0.3	
Current	Signal input pin (IN+, IN-) <sup>(2)</sup>	-10	10	mA
	Signal output pin <sup>(3)</sup>	-55	55	mA
	Output short-circuit <sup>(4)</sup>	Continuous		
Temperature	Operating range, $T_A$	-40	125	°C
	Junction, $T_J$ <sup>(5)</sup>	-40	150	
	Storage, $T_{stg}$	-65	150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.3V beyond the supply rails should be current-limited to 10mA or less.

(3) Output terminals are diode-clamped to the power-supply rails. Output signals that can swing more than 0.3V beyond the supply rails should be current-limited to  $\pm 55$ mA or less.

(4) Short-circuit from output to  $V_{CC}$  can cause excessive heating and eventual destruction.

(5) The maximum power dissipation is a function of  $T_{J(MAX)}$ ,  $R_{\theta JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_{J(MAX)} - T_A) / R_{\theta JA}$ . All numbers apply for packages soldered directly onto a PCB.

### 7.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-Body Model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins <sup>(1)</sup>	$\pm 3000$	V
		Machine Model (MM)	$\pm 500$	

(1) JEDEC document JEP155 states that 500 V HBM allows safe manufacturing with a standard ESD control process.



#### ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 7.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
Supply voltage, $V_S=(V+) - (V-)$	Single-supply	3		32	V
	Dual-supply	$\pm 1.5$		$\pm 16$	

**7.4 Thermal Information: LM393**

THERMAL METRIC <sup>(1)</sup>		LM393	UNIT
		8PINS	
		SOP8	
R <sub>θJA</sub>	Junction-to-ambient thermal resistance	116	°C/W
R <sub>θJC(top)</sub>	Junction-to-case(top) thermal resistance	60	°C/W
R <sub>θJB</sub>	Junction-to-board thermal resistance	56	°C/W
Ψ <sub>JT</sub>	Junction-to-top characterization parameter	12.8	°C/W
Ψ <sub>JB</sub>	Junction-to-board characterization parameter	98.3	°C/W
R <sub>θJC(bot)</sub>	Junction-to-case(bottom) thermal resistance	N/A	°C/W

(1) Thermal resistance varies with operating conditions.

## 7.5 ELECTRICAL CHARACTERISTICS

(At  $T_A = +25^\circ\text{C}$ ,  $V_{CM}=(V_S/2)$ ,  $V_S=5\text{V}$ , unless otherwise noted.)

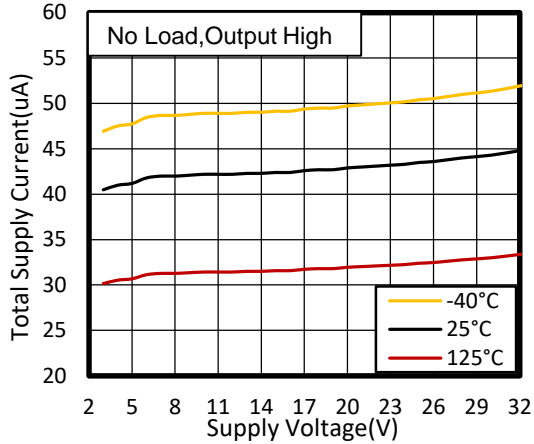
PARAMETER		CONDITIONS	LM393			UNITS
			MIN	TYP	MAX	
$V_S$	Operating Voltage Range		3		32	V
$I_Q$	Quiescent Current	$V_S=5\text{V}$ , no load		40	80	$\mu\text{A}$
		$V_S=32\text{V}$ , no load, $T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$		42	85	
$V_{OS}$	Input offset voltage	$V_S=5\text{V}$ to $32\text{V}$	-4.5	$\pm 1$	4.5	mV
		$V_S=5\text{V}$ to $32\text{V}$ $T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$	-5		5	
$I_B$	Input Bias Current	$T_A=25^\circ\text{C}$		10	50	pA
		$T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$			100	nA
$I_{OS}$	Input Offset Current	$T_A=25^\circ\text{C}$		10	50	pA
		$T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$			100	nA
$V_{CM}$	Common-Mode Voltage Range	$V_S=3\text{V}$ to $32\text{V}$	(V-)		(V+)-1.5	V
		$V_S=3\text{V}$ to $32\text{V}$ $T_A=-40^\circ\text{C}$ to $+125^\circ\text{C}$	(V-)		(V+)-2.0	
$A_{VD}$	Large signal differential voltage amplification	$V_S=15\text{V}$ , $V_O=1.4\text{V}$ to $11.4\text{V}$ , $R_L \geq 15\text{k}$ to (V+)	50	200		V/mV
$V_{OL}$	Low-Level output voltage	$I_{sink} \leq 4\text{mA}$ , $V_{ID}=-1\text{V}$		210	300	mV
$I_{OL}$	Output Current(sinking)	$V_O=1.5\text{V}$ ; $V_{ID}=-1\text{V}$ ; $V_S=5\text{V}$		27		mA
$I_{LEAK}$	High-Level Output Leakage Current	(V+) = $V_O=5\text{V}$ ; $V_{ID}=1\text{V}$		2	20	nA
		(V+) = $V_O=32\text{V}$ ; $V_{ID}=1\text{V}$		4	50	nA
<b>Switching Characteristics</b>						
$T_{PHL}$	Propagation Delay H To L	$V_S=5\text{V}$	$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $10\text{mV}$		7.8	us
			$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $100\text{mV}$		2.0	
		$V_S=32\text{V}$	$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $10\text{mV}$		11.5	
			$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $100\text{mV}$		3.0	
$T_{PLH}$	Propagation Delay L To H	$V_S=5\text{V}$	$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $10\text{mV}$		28	us
			$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $100\text{mV}$		6.1	
		$V_S=32\text{V}$	$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $10\text{mV}$		29	
			$R_{PU}=5.1\text{k}\Omega$ , Overdrive = $100\text{mV}$		7.1	



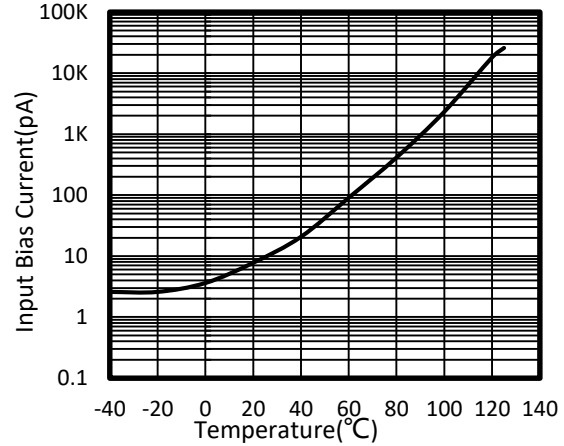
## 7.6 TYPICAL CHARACTERISTICS

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

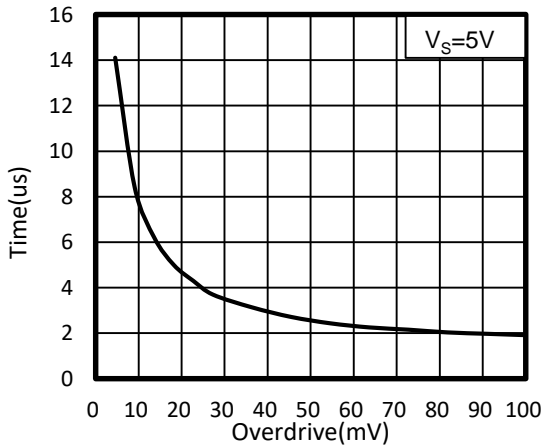
At  $T_A = +25^\circ\text{C}$ ,  $V_S=5\text{V}$ ,  $R_{\text{PULLUP}}=5.1\text{K}$ ,  $V_{\text{CM}} = V_S/2$ ,  $C_L=15\text{pF}$ ,  $V_{\text{OVERDRIVE}}=100\text{mV}$ , unless otherwise noted.



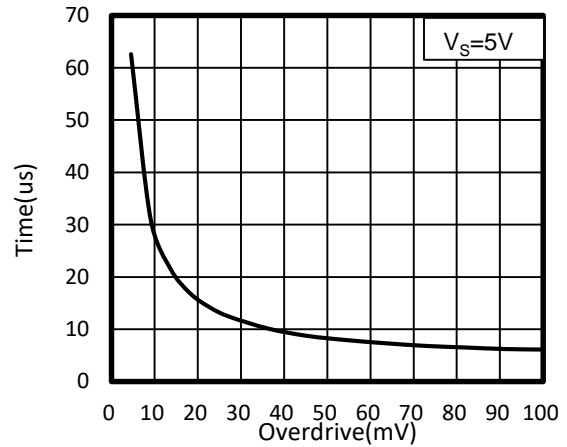
**Figure 1. Total Supply Current vs Supply Voltage**



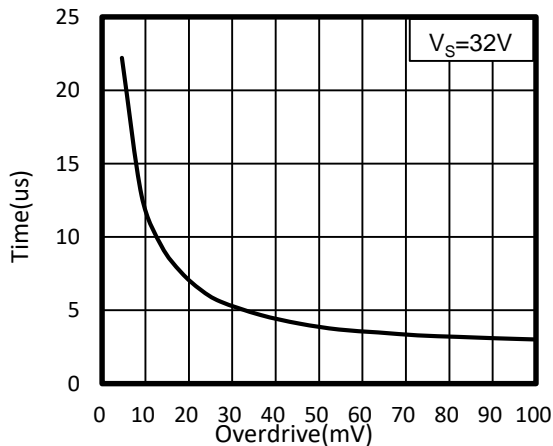
**Figure 2. INPUT BIAS CURRENT vs TEMPERATURE**



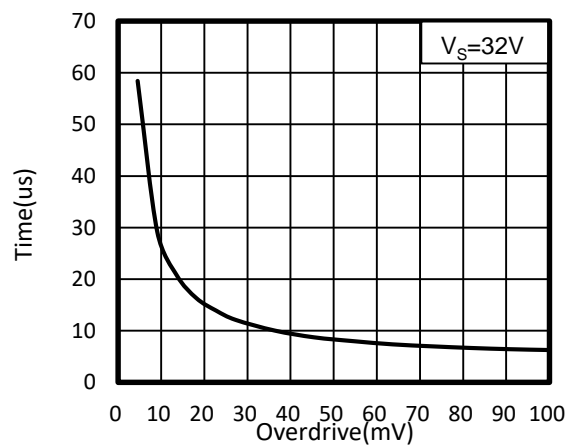
**Figure 3. Response Time vs Input Overdrives Negative Transition**



**Figure 4. Response Time vs Input Overdrives Positive Transition**



**Figure 5. Response Time vs Input Overdrives Negative Transition**

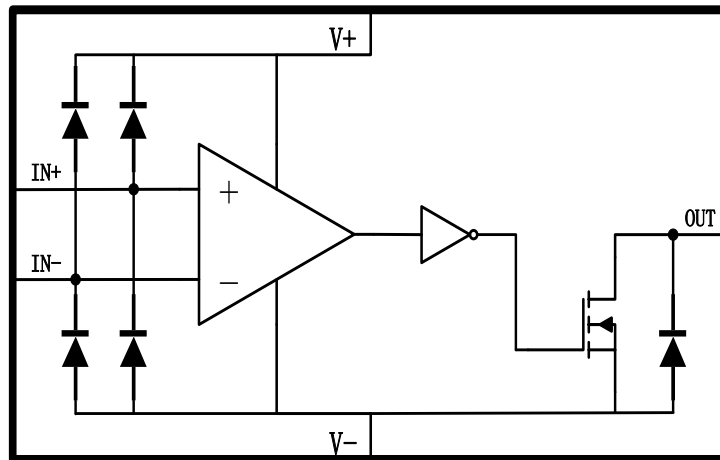


**Figure 6. Response Time vs Input Overdrives Positive Transition**

## 8 Detailed Description

### 8.1 Overview

The LM393 family of comparators can operate up to 32V on the supply pin. This standard device has proven ubiquity and versatility across a wide range of applications. This is due to its low power and high speed. The open-drain output allows the user to configure the output's logic low voltage ( $V_{OL}$ ) and can be utilized to enable the comparator to be used in AND functionality.



**Figure 7. Functional Block Diagram**

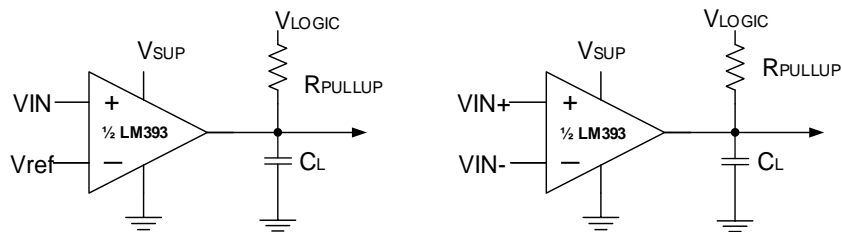
## 9 Application and Implementation

Information in the following applications sections is not part of the Runic component specification, and Runic does not warrant its accuracy or completeness. Runic's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 9.1 Application Information

LM393 is typically used to compare a single signal to a reference or two signals against each other. Many users take advantage of the open drain output (logic high with pull-up) to drive the comparison logic output to a logic voltage level to an MCU or logic device. The wide supply range and high voltage capability makes this comparator optimal for level shifting to a higher or lower voltage.

### 9.2 Typical Application



**Figure 8. Single-Ended and Differential Comparator Configurations**

### 9.3 Detailed Design Procedure

When using the device in a general comparator application, determine the following:

- Input Voltage Range
- Minimum Overdrive Voltage
- Output and Drive Current
- Response Time

### 9.4 Input Voltage Range

When choosing the input voltage range, the input common mode voltage range (VICR) must be taken in to account. If temperature operation is below 25°C the VICR can range from 0 V to  $V_{CC} - 2.0$  V. This limits the input voltage range to as high as  $V_{CC} - 2.0$  V and as low as 0 V. Operation outside of this range can yield incorrect comparisons.

## 10 Layout

### 10.1 Layout Guidelines

For accurate comparator applications without hysteresis it is important maintain a stable power supply with minimized noise and glitches. To achieve this, it is best to add a bypass capacitor between the supply voltage and ground. This should be implemented on the positive power supply and negative supply (if available). If a negative supply is not being used, do not put a capacitor between the IC's GND pin and system ground. Minimize coupling between outputs and inverting inputs to prevent output oscillations. Do not run output and inverting input traces in parallel unless there is a  $V_{CC}$  or GND trace between output and inverting input traces to reduce coupling. When series resistance is added to inputs, place resistor close to the device.

### 10.2 Layout Example

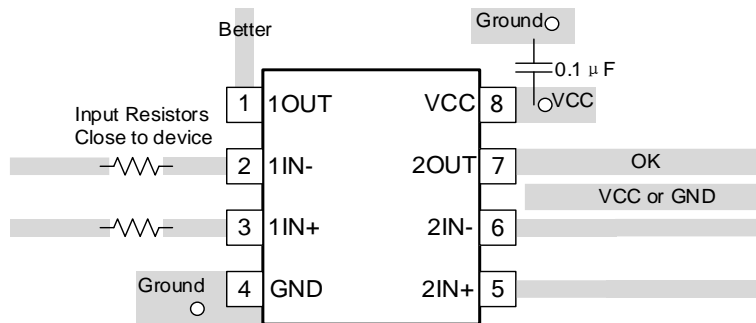
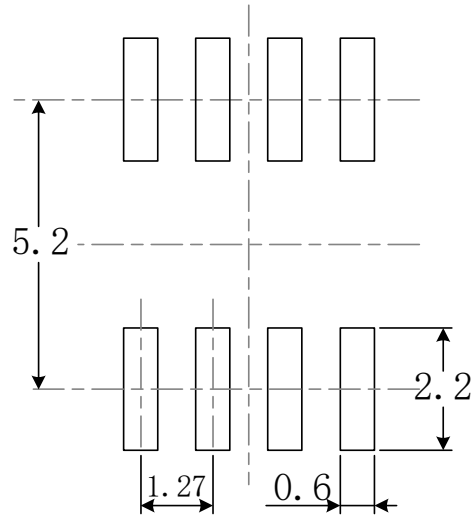
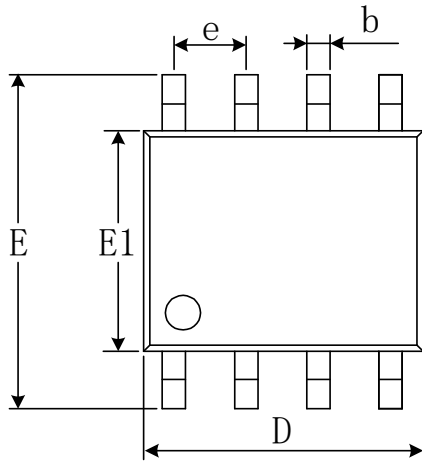
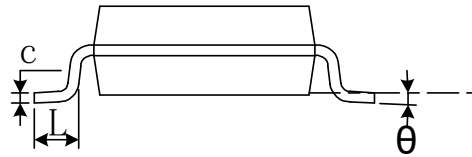
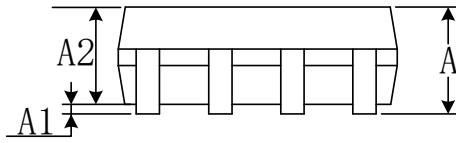


Figure 9. LM393 Layout Example

# 11 PACKAGE OUTLINE DIMENSIONS

## SOP8


**RECOMMENDED LAND PATTERN** (Unit: mm)


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270(BSC)		0.050(BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°

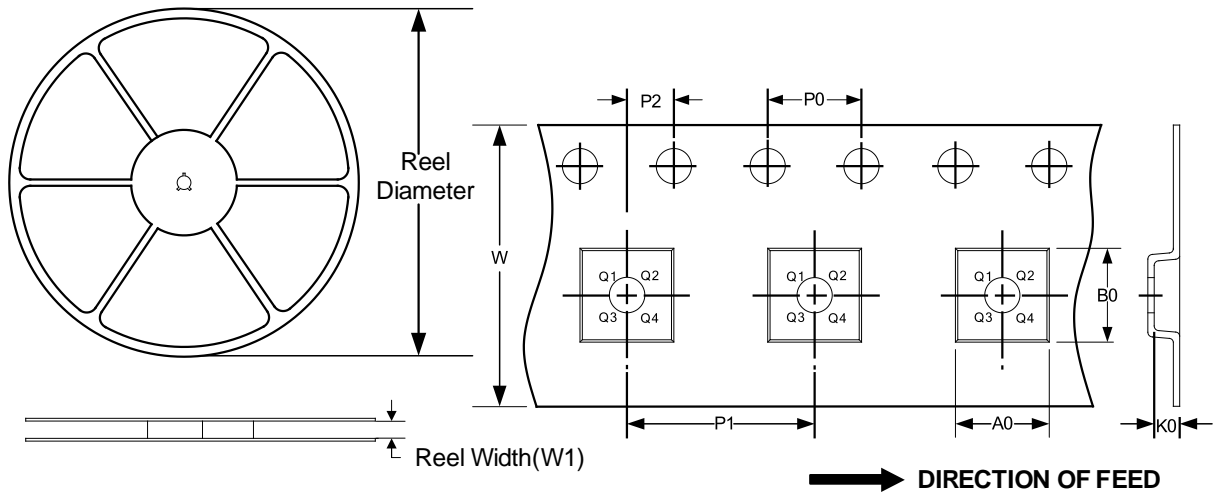
**NOTE:**

- A. All linear dimension is in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
- D. BSC: Basic Dimension. Theoretically exact value shown without tolerances.

## 12 TAPE AND REEL INFORMATION

### REEL DIMENSIONS

### TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOP8	13"	12.4	6.40	5.40	2.10	4.0	8.0	2.0	12.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

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