

4Ω, 400MHz Bandwidth, Dual-Channel SPDT Analog Switch with Negative Signaling Capability

FEATURES

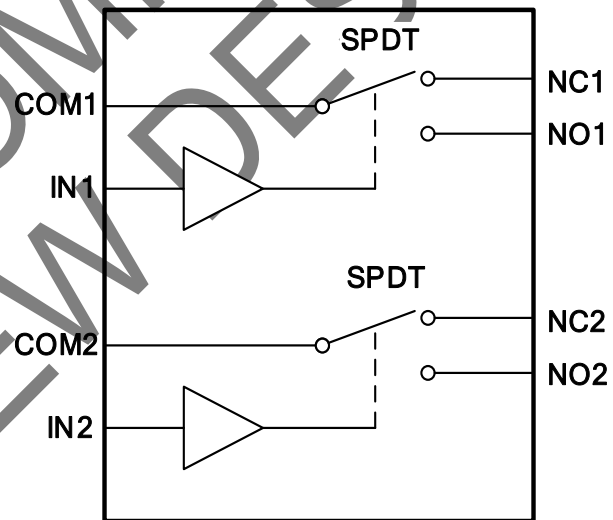
- **-3dB Bandwidth: 400MHz**
- **Supply Range: +2.5V to +5.5V**
- **Negative Signal Swing Capability: -2V to V₊**
- **Break-Before-Make Switching**
- **Fast t_{ON}, t_{OFF} Times**
- **1.8V Logic Control**
- **Extended Industrial Temperature Range: -40°C to +85°C**
- **Small Package Available in Green UQFN1.4X1.8-10 Package**

DESCRIPTION

The RS2117 is a bidirectional, dual-channel single-pole double-throw (SPDT) analog switch that is designed to operate from 2.5V to 5.5V, and the switches can handle negative signal down to -2.0V.

The device also offers a low ON-state resistance of 4Ω (typical), which is matched to within 1 Ω between channels. This device is available packaged in UQFN1.4X1.8-10.

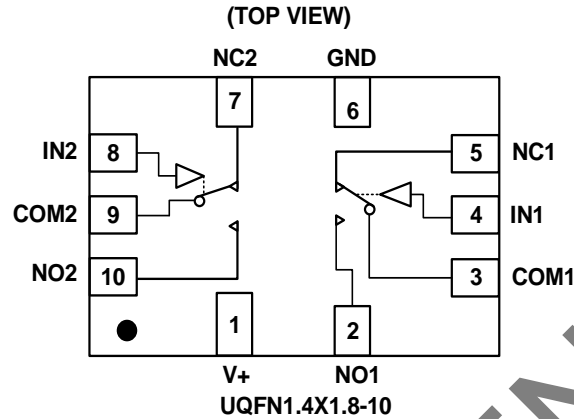
Functional Block Diagram



APPLICATIONS

- **Wearable Devices**
- **Battery-Operated Equipment**
- **Signal Gating, Chopping, Modulation or Demodulation (Modem)**
- **Portable Computing**
- **Cell Phones**

PIN CONFIGURATIONS



PIN DESCRIPTION

NAME	PIN	FUNCTION
V+	1	Power Supply
NO1, NO2	2,10	Normally-Open Terminal
COM1, COM2	3,9	Common Terminal
IN1, IN2	4,8	Digital Control Pin
NC1, NC2	5,7	Normally-Closed Terminal
GND	6	Ground

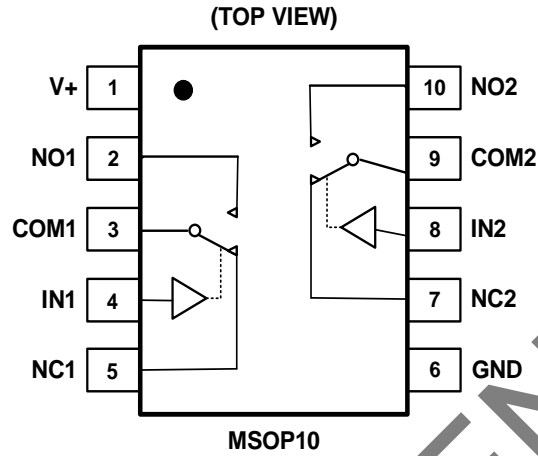
NOTE: NOX, NCX and COMX terminals may be an input or output.

FUNCTION TABLE

LOGIC	NO	NC
0	OFF	ON
1	ON	OFF

NOTE: Switches shown for logic "0" input.

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SPECIFICATIONS

Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) ⁽¹⁾

SYMBOL	PARAMETER	MIN	MAX	UNIT
V ₊	Supply Voltage	-0.3	6.0	V
V _{IN}	Input Voltage ⁽²⁾	-0.3	6.0	
	Analog Voltage Range	-2.0	(V ₊)+0.3	
	Digital Voltage Range	-0.3	(V ₊)+0.3	
	Continuous Current NO, NC, or COM	-100	+100	mA
I _{PEAK}	Peak Current NO, NC, or COM	-150	+150	
T _J	Junction Temperature		150	°C
T _{stg}	Storage temperature	-65	+150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) Input terminals are diode-clamped to the power-supply rails. Input signals that can swing more than 0.3V beyond the supply rails should be current-limited to 10mA or less.

ESD Ratings

		VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human-body model (HBM)	±2000
		Machine Model (MM)	±200

Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted) ⁽³⁾

SYMBOL	PARAMETER	MIN	MAX	UNIT
V _{CC}	Supply Voltage	2.5	5.5	V
T _A	Operating temperature	-40	+85	°C

Thermal Information

THERMAL METRIC		RS2117		UNIT
		10 PINS		
		MSOP10	UQFN1.4X1.8-10	
R _{θJA}	Junction-to-ambient thermal resistance	180.7	120	°C/W
R _{θJC(top)}	Junction-to-case(top) thermal resistance	66.2	46.0	°C/W
R _{θJB}	Junction-to-board thermal resistance	103.2	44.5	°C/W
Ψ _{JT}	Junction-to-top characterization parameter	11.2	1.5	°C/W
Ψ _{JB}	Junction-to-board characterization parameter	101.3	44.5	°C/W
R _{θJC(bot)}	Junction-to-case(bottom) thermal resistance	N/A	31.2	°C/W

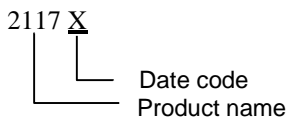
PACKAGE/ORDERING INFORMATION

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ^(1/2)	PACKAGE OPTION
RS2117	RS2117YUTQK10	-40°C ~+85°C	UQFN1.4X1.8-10	2117X	Tape and Reel,4000
RS2117	RS2117YN	-40°C ~+85°C	MSOP10	RS2117	Tape and Reel,4000

NOTE:

- (1) There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.
- (2) X = Date Code.

MARKING INFORMATION



NOT RECOMMENDED FOR NEW DESIGN

ELECTRICAL CHARACTERISTICS

$V_+ = 5.0\text{ V}$, $T_A = -40^\circ\text{C}$ to 85°C (unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	V+	T _A	MIN	TYP	MAX	UNITS
ANALOG SWITCH								
Analog Signal Range	V_{NO} , V_{NC} , V_{COM}	$2.5\text{V} \leq V_+ \leq 3.5\text{V}$ $3.5\text{V} \leq V_+ \leq 5.5\text{V}$		FULL	-2.0 (V ₊)-5.5		V ₊ V ₊	V
On-Resistance	R _{ON}	$0 \leq (V_{NO}$ or $V_{NC}) \leq V_+$, $I_{COM} = -10\text{mA}$, Switch ON, See Figure 1	5V	+25°C		4	7	Ω
				FULL			7.5	Ω
			3.3V	+25°C		6	9	Ω
				FULL				9.5
On-Resistance Match Between Channels	ΔR _{ON}	$0 \leq (V_{NO}$ or $V_{NC}) \leq V_+$, $I_{COM} = -10\text{mA}$, Switch ON, See Figure 1	5V	+25°C		0.15	0.5	Ω
				FULL			0.6	Ω
			3.3V	+25°C		0.25	0.85	Ω
				FULL				1.0
On-Resistance Flatness	R _{FLAT(ON)}	$0 \leq (V_{NO}$ or $V_{NC}) \leq V_+$, $I_{COM} = -10\text{mA}$, Switch ON, See Figure 1	5V	+25°C		1	2	Ω
				FULL			2.5	Ω
			3.3V	+25°C		3	5	Ω
				FULL				5.4
NC, NO OFF Leakage Current	I _{NC(OFF)} , I _{NO(OFF)}	V_{NO} or $V_{NC} = 0.3\text{V}$, $V_+/2$ $V_{COM} =$ $V_+/2$, 0.3V See Figure 2	2.5V to 5.5V	FULL			1	μA
NC, NO, COM ON Leakage Current	I _{NC(ON)} , I _{NO(ON)} , I _{COM(ON)}	V_{NO} or $V_{NC} = 0.3\text{V}$, Open $V_{COM} =$ Open, 0.3V See Figure 3	2.5V to 5.5V	FULL			1	μA
DIGITAL CONTROL INPUTS⁽¹⁾								
Input High Voltage	V _{INH}		5V	FULL	1.5			V
			3.3V	FULL	1.3			V
Input Low Voltage	V _{INL}		5V	FULL			0.5	V
			3.3V	FULL			0.4	V
Input Leakage Current	I _{IN}	V _{IN} = V _{IO} or 0	2.5V to 5.5V	FULL			1	μA

(1) All unused digital inputs of the device must be held at V_{IO} or GND to ensure proper device operation.

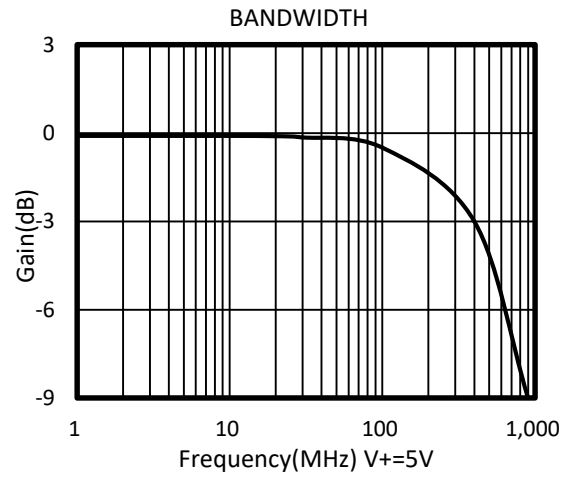
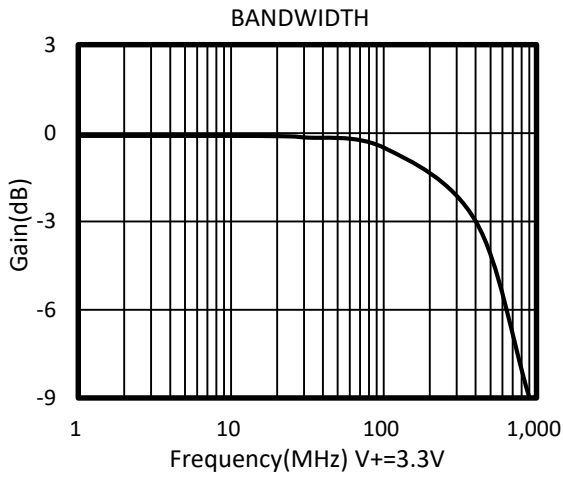
ELECTRICAL CHARACTERISTICS (continued)

$V_+ = 5.0\text{ V}$, TEMP = -40°C to 85°C (unless otherwise noted)

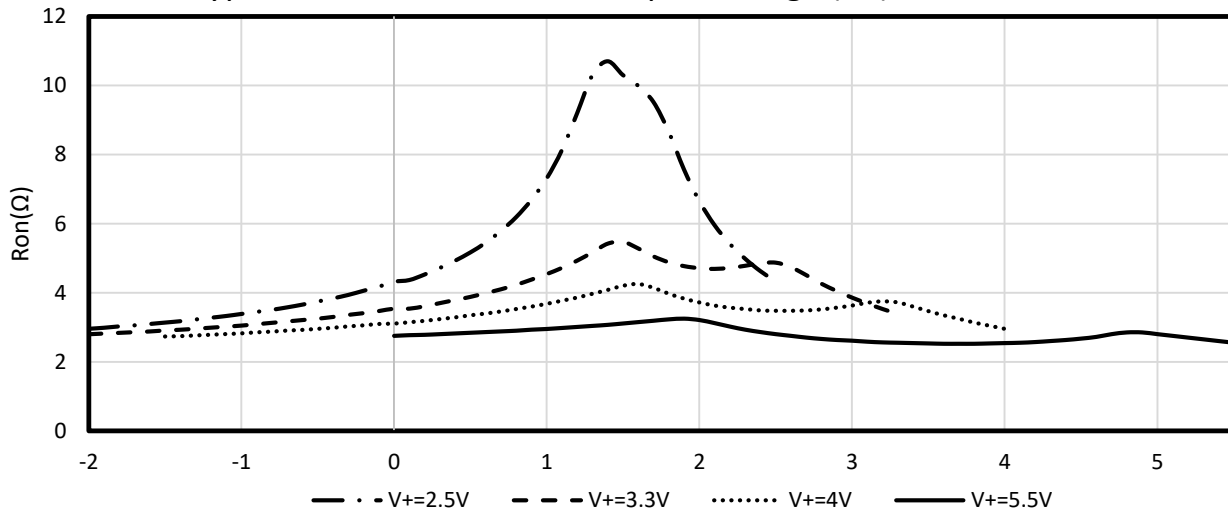
PARAMETER	SYMBOL	CONDITIONS	V+	TEMP	MIN	TYP	MAX	UNITS
DYNAMIC CHARACTERISTICS								
Turn-On Time	t_{ON}	$V_{COM} = V_+$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, See Figure 5	5V	+25°C		15		ns
			3.3V			25		
Turn-Off Time	t_{OFF}	$V_{COM} = V_+$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, See Figure 5	5V	+25°C		10		ns
			3.3V			15		
Break-Before-Make Time Delay	t_{BBM}	$V_{NO1} = V_{NC1} = V_{NO2} = V_{NC2} = V_+/2$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, See Figure 6	5V	+25°C		5		ns
			3.3V			10		
Charge Injection	Q	$V_G = \text{GND}$, $R_G = 0\Omega$, $C_L = 1.0\text{nF}$, See Figure 10	5V	+25°C		15		pC
			3.3V	+25°C		10		
Off Isolation	O_{ISO}	$R_L = 50\Omega$, Switch OFF, See Figure 8	f = 1MHz	+25°C		-70		dB
			f = 10MHz	+25°C		-50		dB
-3dB Bandwidth	BW	Switch ON, $R_L = 50\Omega$ See Figure 7		+25°C		400		MHz
Channel-to-Channel Crosstalk	X_{TALK}	Signal=0dBm, $R_L = 50\Omega$, $C_L = 5\text{pF}$, See Figure 9	f = 1MHz	+25°C		-72		dB
			f = 10MHz	+25°C		-52		dB
NC, NO OFF Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$	V_{NC} or $V_{NO} = V_+/2$ or GND, Switch OFF See Figure 4		+25°C		10		pF
NC, NO, COM ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$	V_{NC} or $V_{NO} = V_+/2$ or GND, Switch ON See Figure 4		+25°C		25		pF
POWER REQUIREMENTS								
Power Supply Range	V_+			FULL	2.5		5.5	V
Power Supply Current	I_+	$V_{IN} = \text{GND}$ or V_+	5.5V	FULL			1	μA

NOT RECOMMENDED FOR NEW DESIGN

TYPICAL CHARACTERISTICS



Typical R_{on} as a Function of Input Voltage (V_I) for $V_I = 0$ to V_+



NO FOR

Parameter Measurement Information

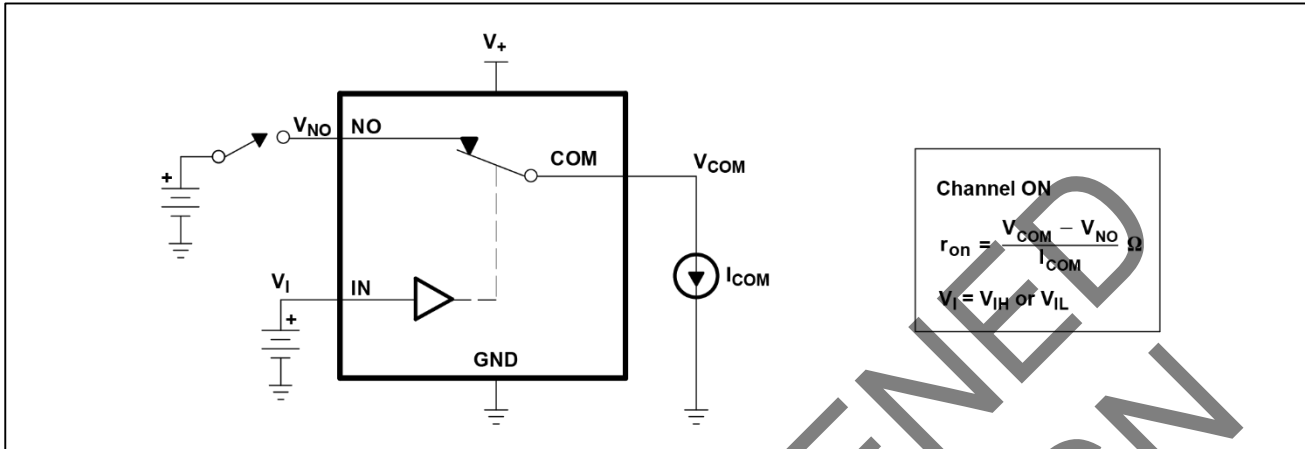


Figure 1.ON-State Resistance (r_{on})

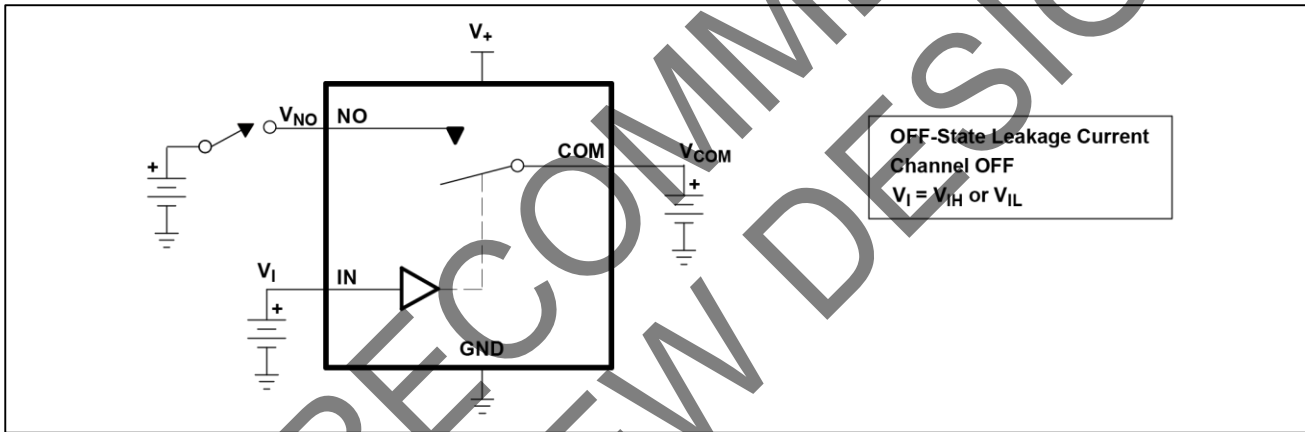


Figure 2.OFF-State Leakage Current ($I_{COM(OFF)}$, $I_{NO(OFF)}$)

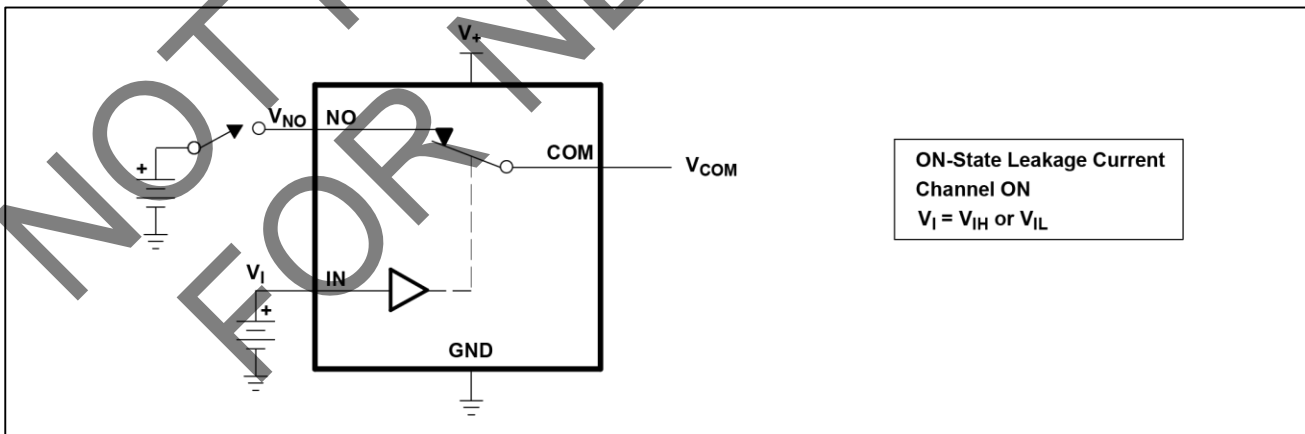


Figure 3.ON-State Leakage Current ($I_{COM(ON)}$, $I_{NO(ON)}$)

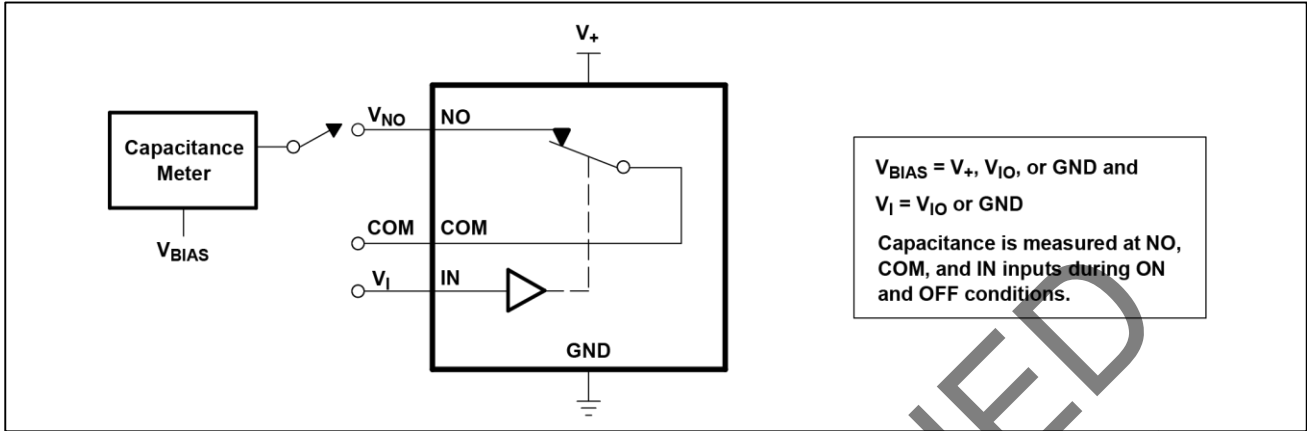


Figure 4. Capacitance (C_I , $C_{COM(OFF)}$, $C_{COM(ON)}$, $C_{NO(OFF)}$, $C_{NO(ON)}$)

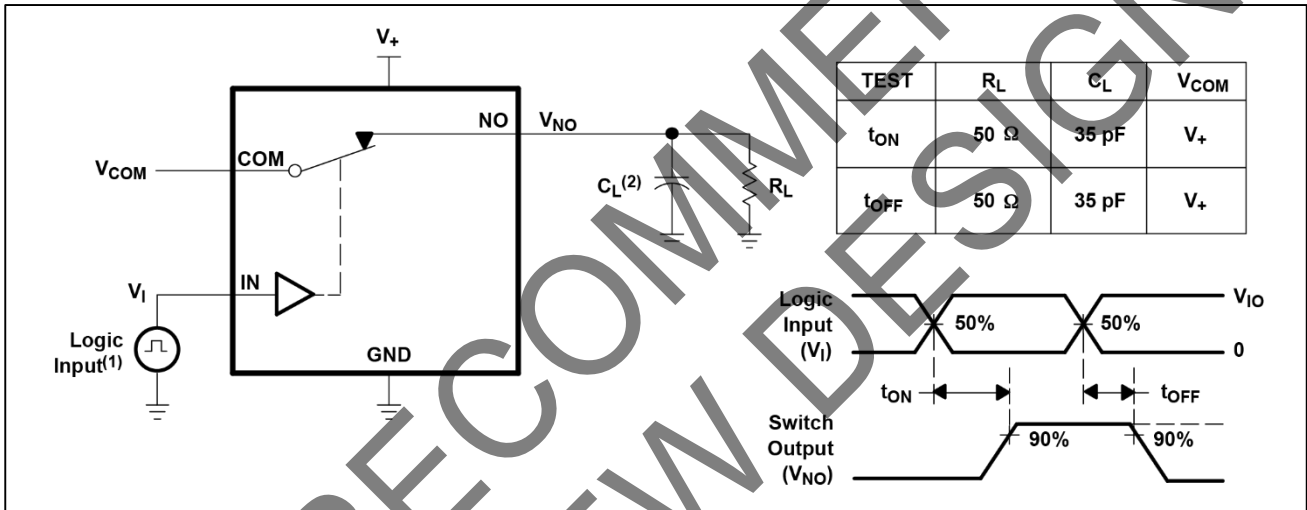


Figure 5. Turn-On (t_{ON}) and Turn-Off Time (t_{OFF})

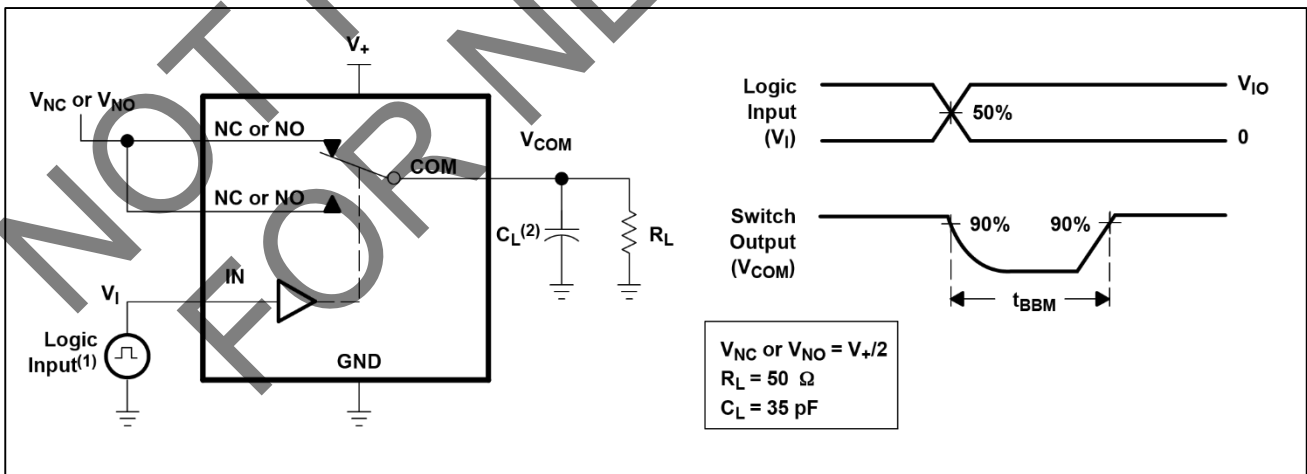


Figure 6. Break-Before-Make Time (t_{BBM})

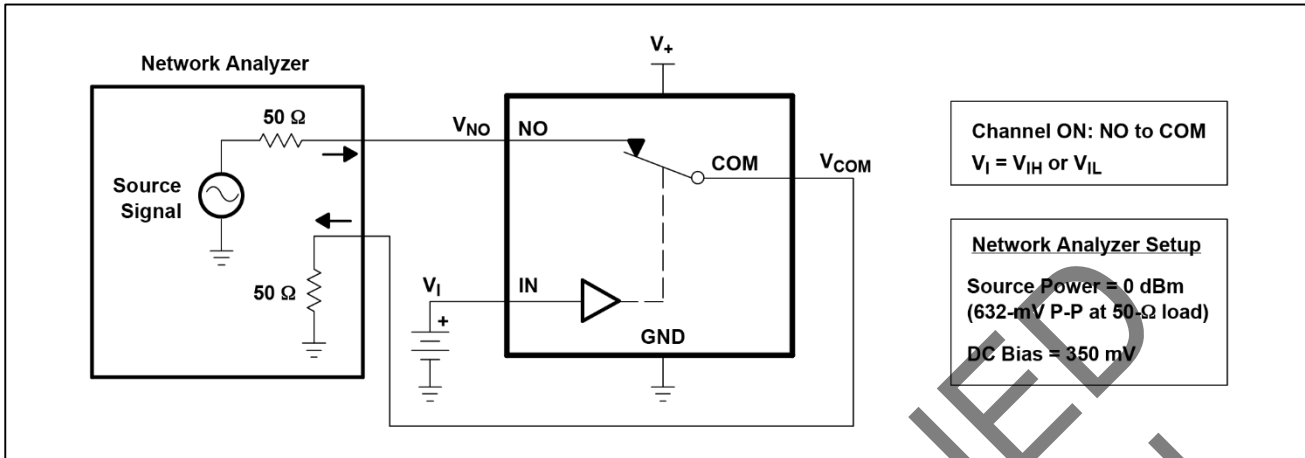


Figure 7. Bandwidth (BW)

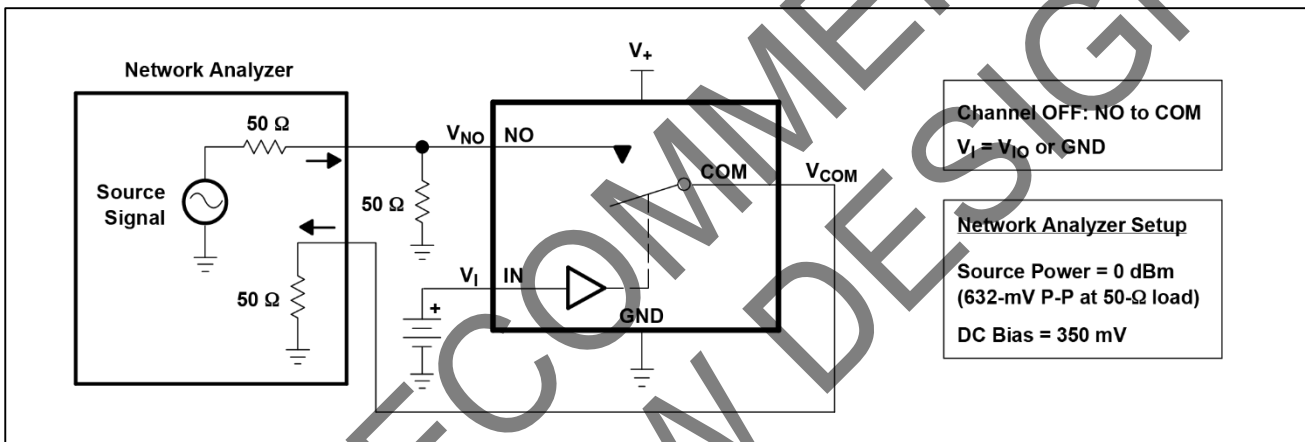


Figure 8. OFF Isolation (O_{iso})

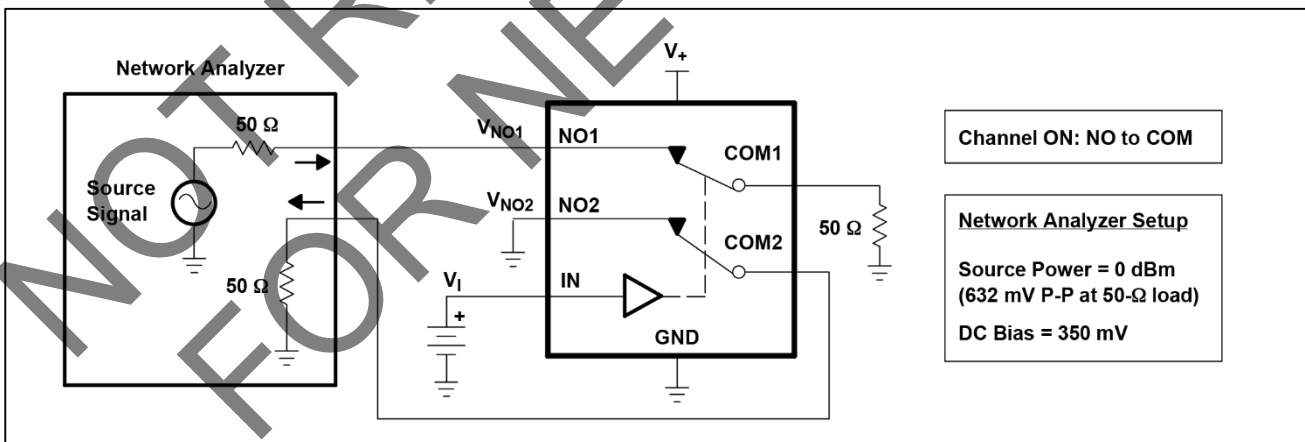


Figure 9. Crosstalk (X_{TALK})

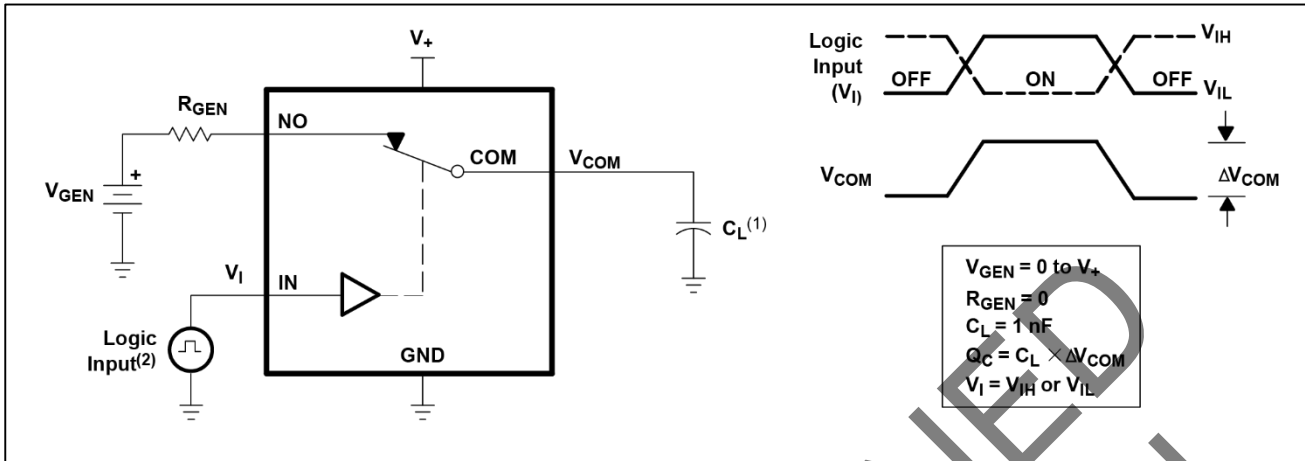


Figure 10. Charge Injection (Q_c)

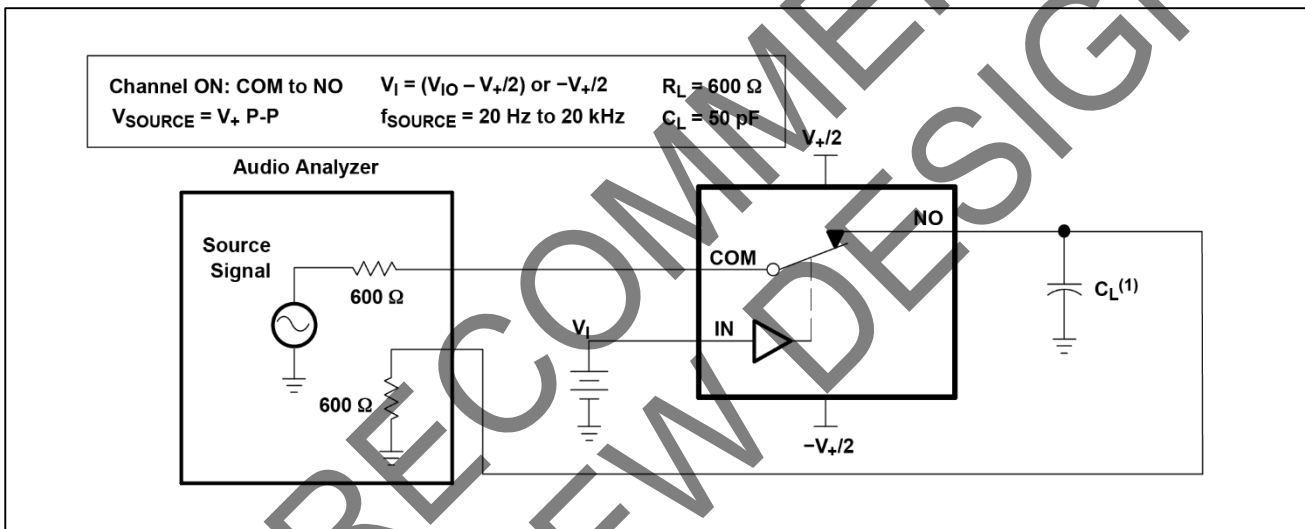


Figure 11. Total Harmonic Distort

NOT RECOMMENDED FOR NEW DESIGN

TYPICAL APPLICATION

Analog signals that range over the entire supply voltage V_{CC} to GND can be passed with very little change in ON-state resistance. The switches are bidirectional, so the NO, NC, and COM pins can be used as either inputs or outputs. Pull the digitally controlled input select pin IN to V_{CC} or GND to avoid unwanted switch states that could result if the logic control pin is left floating.

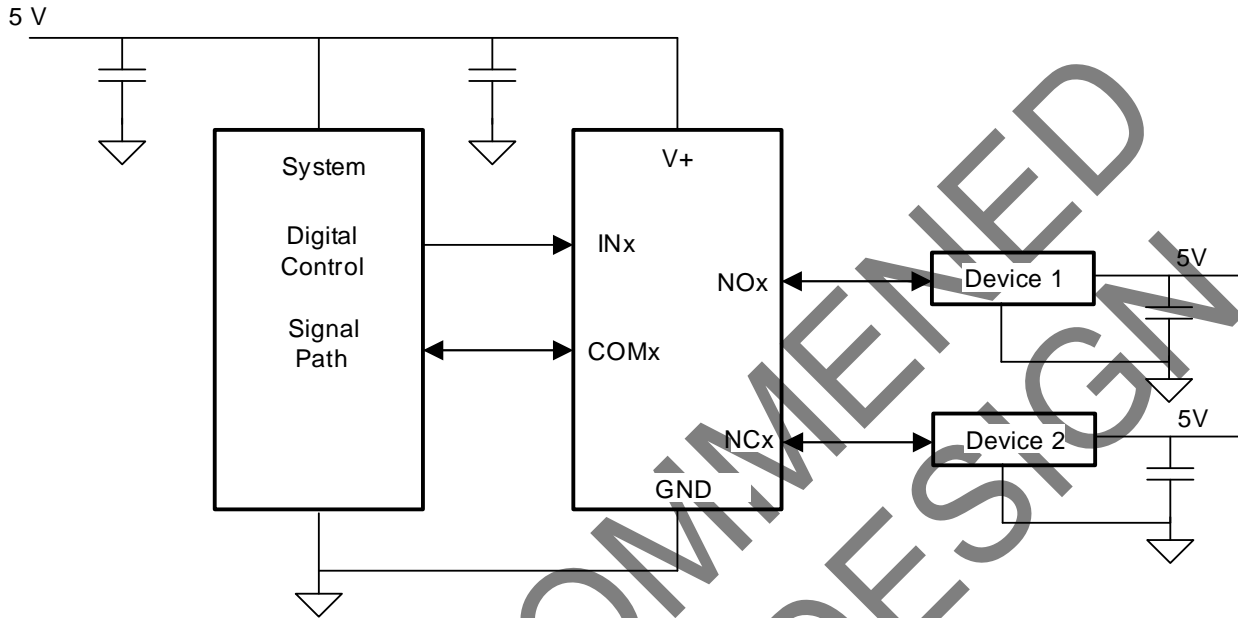
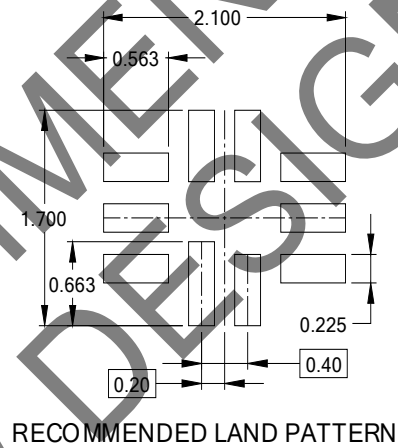
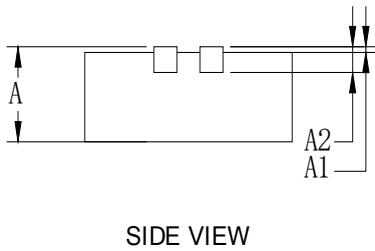
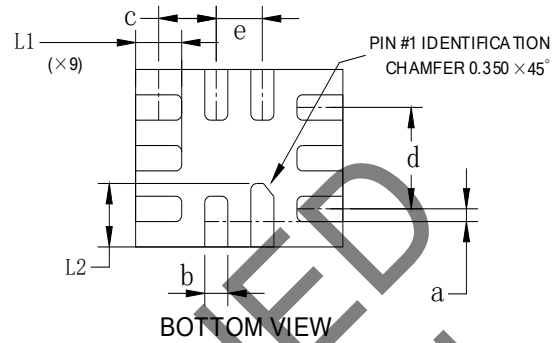
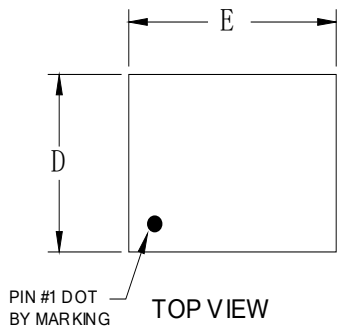


Figure 12. Typical Application Schematic

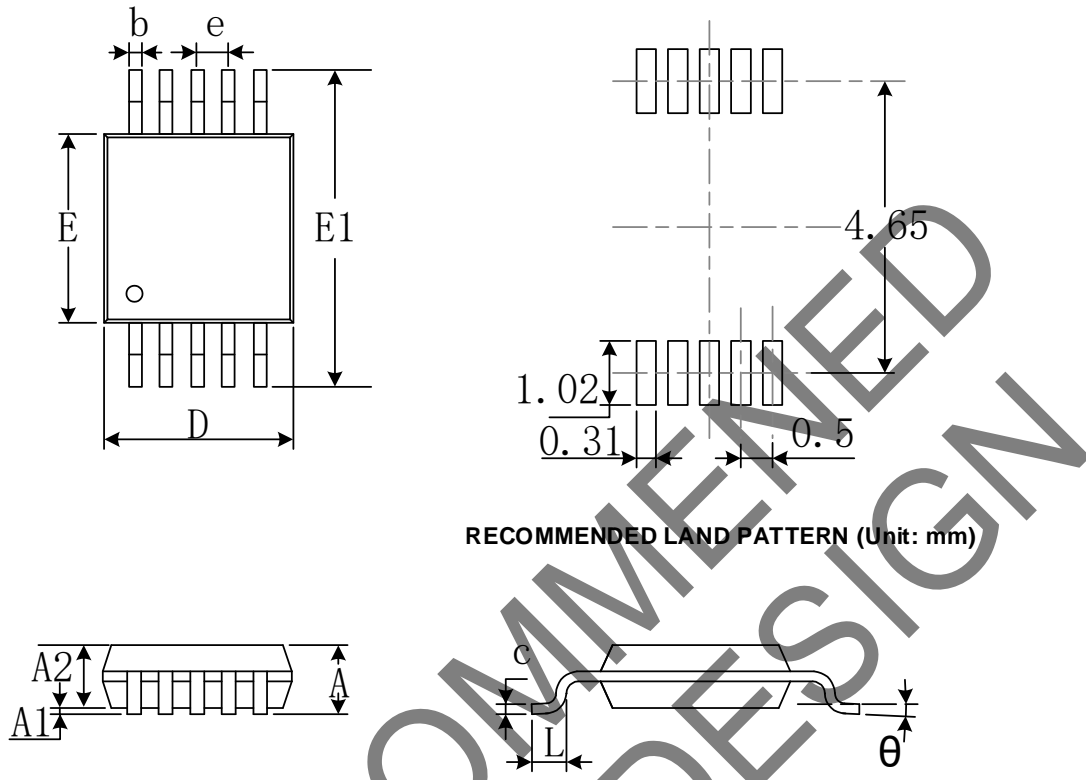
NOT RECOMMENDED FOR NEW DESIGN

PACKAGE OUTLINE DIMENSIONS UQFN1.4X1.8-10



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.500	0.600	0.020	0.024
A1	0.000	0.050	0.000	0.002
A2	0.203 REF		0.008 REF	
a	0.050	0.150	0.002	0.006
b	0.150	0.250	0.006	0.010
c	0.450	0.550	0.018	0.022
d	0.800 REF		0.031 REF	
D	1.350	1.450	0.053	0.057
E	1.750	1.850	0.069	0.073
e	0.400 TYP		0.016 TYP	
L1	0.350	0.450	0.014	0.018
L2	0.450	0.550	0.018	0.022

MSOP10



RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.820	1.100	0.032	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.180	0.280	0.007	0.011
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
e	0.50(BSC)		0.020(BSC)	
E	2.900	3.100	0.114	0.122
E1	4.750	5.050	0.187	0.199
L	0.400	0.800	0.016	0.031
θ	0°	6°	0°	6°