

# RS1G175 Single D-Type Flip-Flop with Asynchronous Clear

## 1 FEATURES

- **Operating Voltage Range: 1.65V to 5.5V**
- **Low Power Consumption: 10 $\mu$ A (Max)**
- **Operating Temperature Range: -40°C to +125°C**
- **Inputs Accept Voltage to 5.5V**
- **High Output Drive:  $\pm$ 24mA at V<sub>CC</sub>=3.0V**
- **I<sub>off</sub> Supports Live Insertion, Partial-Power-Down Mode, and Back-Drive Protection**
- **Micro Size Packages: SOT23-6, SC70-6**

## 2 APPLICATIONS

- **Enterprise Switching**
- **Telecom Infrastructure**
- **Servers**
- **Medical Healthcare and Fitness**
- **PC/Notebook**
- **Motor Drives**

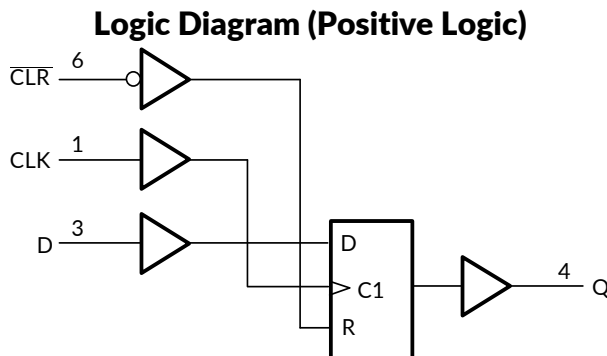
## 3 DESCRIPTIONS

The RS1G175 single D-type flip-flop is designed for 1.65V to 5.5V V<sub>CC</sub> operation.

The RS1G175 device has an asynchronous clear ( $\overline{\text{CLR}}$ ) input. When  $\overline{\text{CLR}}$  is high, data from the input pin (D) is transferred to the output pin (Q) on the clock's (CLK) rising edge. When  $\overline{\text{CLR}}$  is low, Q is forced into the low state, regardless of the clock edge or data on D.

The RS1G175 is fully specified for partial-power-down applications using I<sub>off</sub>. The I<sub>off</sub> circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

This device available in Green SOT23-6 and SC70-6 packages. It operates over an ambient temperature range of -40°C to +125°C.



### Device Information <sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS1G175	SC70-6	2.10mm×1.25mm
	SOT23-6	1.60mm×2.92mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

## Table of Contents

<b>1 FEATURES</b> .....	1
<b>2 APPLICATIONS</b> .....	1
<b>3 DESCRIPTIONS</b> .....	1
<b>4 REVISION HISTORY</b> .....	3
<b>5 PACKAGE/ORDERING INFORMATION</b> <sup>(1)</sup> .....	4
<b>6 PIN CONFIGURATIONS</b> .....	5
6.1 Pin Description .....	5
6.2 Function Table .....	5
<b>7 SPECIFICATIONS</b> .....	6
7.1 Absolute Maximum Ratings .....	6
7.2 ESD Ratings .....	6
<b>8 ELECTRICAL CHARACTERISTICS</b> .....	7
8.1 Recommended Operating Conditions .....	7
8.2 DC Characteristics .....	8
8.3 Timing Requirements .....	8
8.4 Switching Characteristics .....	9
8.5 Switching Characteristics .....	9
8.6 Operating Characteristics .....	9
8.7 Typical Characteristics .....	9
<b>9 PARAMETER MEASUREMENT INFORMATION</b> .....	10
<b>10 DETAILED DESCRIPTION</b> .....	11
10.1 Overview .....	11
10.2 Functional Block Diagram .....	11
10.3 Feature Description .....	11
<b>11 APPLICATION AND IMPLEMENTATION</b> .....	12
11.1 Application Information .....	12
11.2 Typical Application .....	12
11.3 Design Requirements .....	12
<b>12 POWER SUPPLY RECOMMENDATIONS</b> .....	12
<b>13 LAYOUT</b> .....	13
13.1 Layout Guidelines .....	13
13.2 Layout Example .....	13
<b>14 PACKAGE OUTLINE DIMENSIONS</b> .....	14
<b>15 TAPE AND REEL INFORMATION</b> .....	16

## 4 REVISION HISTORY

Note: Page numbers for previous revisions may differ from page numbers in the current version.

Version	Change Date	Change Item
A.0	2023/05/09	Preliminary version completed
A.1	2023/06/28	Update Timing Requirements and Switching Characteristics
A.1.1	2024/02/28	Modify packaging naming
A.2	2024/04/16	1. Add MSL on Page 5@RevA.1.1 2. Update PACKAGE note

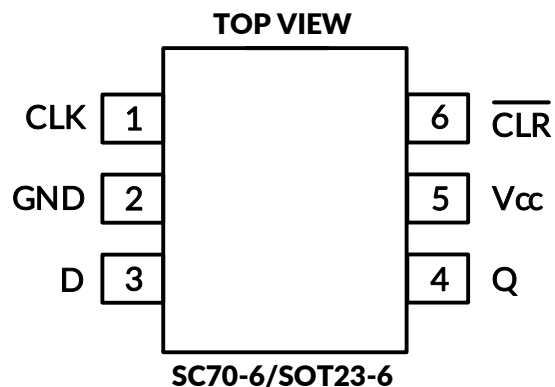
**5 PACKAGE/ORDERING INFORMATION <sup>(1)</sup>**

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING <sup>(2)</sup>	MSL <sup>(3)</sup>	PACKAGE OPTION
RS1G175	RS1G175XC6	-40°C ~+125°C	SC70-6 <sup>(4)</sup>	1G175	MSL3	Tape and Reel, 3000
	RS1G175XH6	-40°C ~+125°C	SOT23-6	1G175	MSL3	Tape and Reel, 3000

## NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.
- (4) Equivalent to SOT363.

## 6 PIN CONFIGURATIONS



### 6.1 Pin Description

PIN	NAME	I/O TYPE <sup>(1)</sup>	FUNCTION
<b>SC70-6/SOT23-6</b>			
1	CLK	I	Clock Input
2	GND	P	Ground
3	D	I	Input
4	Q	O	Output
5	V <sub>cc</sub>	P	Power pin
6	$\overline{\text{CLR}}$	I	Clear Data Input

(1) I=input, O=output, P=power.

### 6.2 Function Table

INPUTS			OUTPUT
$\overline{\text{CLR}}$	CLK	D	Q
H	↑	L	L
H	↑	H	H
H	H or L	X	Q <sub>0</sub>
L	X	X	L

(1) H=High Voltage Level  
L=Low Voltage Level  
X=Don't Care

## 7 SPECIFICATIONS

### 7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted) <sup>(1)(2)</sup>

		MIN	MAX	UNIT
V <sub>CC</sub>	Supply voltage range	-0.5	6.5	V
V <sub>I</sub>	Input voltage range <sup>(2)</sup>	-0.5	6.5	V
V <sub>O</sub>	Voltage range applied to any output in the high-impedance or power-off state <sup>(2)</sup>	-0.5	6.5	V
V <sub>O</sub>	Voltage range applied to any output in the high or low state <sup>(2)(3)</sup>	-0.5	V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input clamp current	V <sub>I</sub> <0	-50	mA
I <sub>OK</sub>	Output clamp current	V <sub>O</sub> <0	-50	mA
I <sub>O</sub>	Continuous output current		±50	mA
	Continuous current through V <sub>CC</sub> or GND		±100	mA
θ <sub>JA</sub>	Package thermal impedance <sup>(4)</sup>	SC70-6	265	°C/W
		SOT23-6	230	
T <sub>J</sub>	Junction temperature <sup>(5)</sup>	-65	150	°C
T <sub>stg</sub>	Storage temperature	-65	150	°C

- (1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of V<sub>CC</sub> is provided in the Recommended Operating Conditions table.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of T<sub>J(MAX)</sub>, R<sub>θJA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any ambient temperature is P<sub>D</sub> = (T<sub>J(MAX)</sub> - T<sub>A</sub>) / R<sub>θJA</sub>. All numbers apply for packages soldered directly onto a PCB.

### 7.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

		VALUE	UNIT
V <sub>(ESD)</sub>	Electrostatic discharge	Human-Body Model (HBM)	±2000
		Charged-Device Model (CDM)	±1000
		Machine Model (MM)	±200



#### ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## 8 ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range (TYP values are at  $T_A = +25^\circ\text{C}$ , Full= $-40^\circ\text{C}$  to  $125^\circ\text{C}$ , unless otherwise noted.)<sup>(1)</sup>

### 8.1 Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
Supply Voltage	$V_{CC}$	Operating	1.65	5.5	V
High-Level Input Voltage	$V_{IH}$	$V_{CC}=1.65\text{V to }1.95\text{V}$	$0.75 \times V_{CC}$		V
		$V_{CC}=2.3\text{V to }2.7\text{V}$	1.7		
		$V_{CC}=3\text{V to }3.6\text{V}$	2		
		$V_{CC}=4.5\text{V to }5.5\text{V}$	$0.7 \times V_{CC}$		
Low-Level Input Voltage	$V_{IL}$	$V_{CC}=1.65\text{V to }1.95\text{V}$		$0.25 \times V_{CC}$	V
		$V_{CC}=2.3\text{V to }2.7\text{V}$		0.7	
		$V_{CC}=3\text{V to }3.6\text{V}$		0.8	
		$V_{CC}=4.5\text{V to }5.5\text{V}$		$0.3 \times V_{CC}$	
Input Voltage	$V_I$		0	5.5	V
Output Voltage	$V_O$		0	$V_{CC}$	V
High-Level Output Current	$I_{OH}$	$V_{CC}=1.65\text{V}$		-4	mA
		$V_{CC}=2.3\text{V}$		-8	
		$V_{CC}=3\text{V}$		-16	
		$V_{CC}=4.5\text{V}$		-24	
Low-Level Output Current	$I_{OL}$	$V_{CC}=1.65\text{V}$		4	mA
		$V_{CC}=2.3\text{V}$		8	
		$V_{CC}=3\text{V}$		16	
		$V_{CC}=4.5\text{V}$		24	
Input Transition Rise or Fall	$\Delta t / \Delta v$	$V_{CC}=1.8\text{V} \pm 0.15\text{V}, 2.5\text{V} \pm 0.2\text{V}$		20	ns/V
		$V_{CC}=3.3\text{V} \pm 0.3\text{V}$		10	
		$V_{CC}=5\text{V} \pm 0.5\text{V}$		10	
Operating Temperature	$T_A$		-40	+125	$^\circ\text{C}$

(1) All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation.

## 8.2 DC Characteristics

PARAMETER		TEST CONDITIONS	V <sub>CC</sub>	TEMP	MIN <sup>(2)</sup>	TYP <sup>(3)</sup>	MAX <sup>(2)</sup>	UNIT
V <sub>OH</sub>		I <sub>OH</sub> = -100μA	1.65V to 5.5V	Full	V <sub>CC</sub> -0.1			V
		I <sub>OH</sub> = -4mA	1.65V		1.2			
		I <sub>OH</sub> = -8mA	2.3V		1.9			
		I <sub>OH</sub> = -16mA	3V		2.4			
		I <sub>OH</sub> = -24mA			2.3			
		I <sub>OH</sub> = -32mA	4.5V		3.8			
V <sub>OL</sub>		I <sub>OL</sub> = 100μA	1.65V to 5.5V	Full			0.1	V
		I <sub>OL</sub> = 4mA	1.65V				0.45	
		I <sub>OL</sub> = 8mA	2.3V				0.3	
		I <sub>OL</sub> = 16mA	3V				0.4	
		I <sub>OL</sub> = 24mA					0.55	
		I <sub>OL</sub> = 32mA	4.5V				0.55	
I <sub>i</sub>	Data or Control Inputs	V <sub>I</sub> =5.5V or GND	0V to 5.5V	+25°C		±0.1	±1	μA
				Full			±5	
	I <sub>off</sub>	V <sub>I</sub> or V <sub>O</sub> =5.5V	0	+25°C		±0.1	±1	μA
				Full			±10	
	I <sub>CC</sub>	V <sub>I</sub> =5.5V or GND, I <sub>O</sub> =0	1.65V to 5.5V	+25°C		0.1	1	μA
				Full			10	
	ΔI <sub>CC</sub>	One input at V <sub>CC</sub> -0.6V, Other inputs at V <sub>CC</sub> or GND	3V to 5.5V	Full			500	μA
	C <sub>i</sub> (Input Capacitance)	V <sub>I</sub> = V <sub>CC</sub> or GND	3.3V	+25°C		5.5		pF

(1) All unused inputs of the device must be held at V<sub>CC</sub> or GND to ensure proper device operation.

(2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

## 8.3 Timing Requirements

over recommended operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEMP	V <sub>CC</sub> =1.8V ± 0.15V		V <sub>CC</sub> =2.5V ± 0.2V		V <sub>CC</sub> =3.3V ± 0.3V		V <sub>CC</sub> =5V ± 0.5V		UNIT
				MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
f <sub>clock</sub>			-40°C to 85°C		30		65		100		155	MHz
			-40°C to 125°C						100		155	
t <sub>w</sub>	CLK High or low		-40°C to 85°C	8		4		3		2		ns
			-40°C to 125°C					3		2		
	CLR low		-40°C to 85°C	12		5		3.4		2		
			-40°C to 125°C					3.4		2		
t <sub>su</sub>	Data		-40°C to 85°C	10.4		4.6		3.2		2		
			-40°C to 125°C					3.2		2		
	CLR inactive		-40°C to 85°C	8.4		3.8		2.6		1.8		
			-40°C to 125°C					2.6		1.8		
t <sub>h</sub>			-40°C to 85°C	0.5		0.5		0.5		0.5		
			-40°C to 125°C					0.5		0.5		

(1) This parameter is ensured by design and/or characterization and is not tested in production.



### 8.4 Switching Characteristics

over recommended operating free-air temperature range,  $C_L = 15\text{ pF}$  (unless otherwise noted) <sup>(1)</sup>

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEMP	$V_{CC}=1.8V \pm 0.15V$		$V_{CC}=2.5V \pm 0.2V$		$V_{CC}=3.3V \pm 0.3V$		$V_{CC}=5V \pm 0.5V$		UNIT
				MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
$f_{max}$			-40°C to 85°C	30		65		100		155		MHz
$t_{pd}$	CLK	Q	-40°C to 85°C	2.5	25.5	2	13	1.4	9.5	1	7	ns
	$\overline{\text{CLR}}$	Q	-40°C to 85°C	2.5	23	2	10	1.2	7.5	1	6	

(1) This parameter is ensured by design and/or characterization and is not tested in production.

### 8.5 Switching Characteristics

over recommended operating free-air temperature range,  $C_L = 30\text{ pF}$  or  $50\text{ pF}$  (unless otherwise noted) <sup>(1)</sup>

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEMP	$V_{CC}=1.8V \pm 0.15V$		$V_{CC}=2.5V \pm 0.2V$		$V_{CC}=3.3V \pm 0.3V$		$V_{CC}=5V \pm 0.5V$		UNIT
				MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
$f_{max}$			-40°C to 85°C	30		65		100		155		MHz
			-40°C to 125°C					100		155		
$t_{pd}$	CLK	Q	-40°C to 85°C	2.7	27.5	2.2	14.1	1.6	11	1.5	8.2	ns
			-40°C to 125°C	2.7	28.3	2.2	15	1.6	11.5	1.5	9	
	$\overline{\text{CLR}}$	Q	-40°C to 85°C	2.7	24.5	2.2	11	1.5	9	1.3	7	
			-40°C to 125°C	2.7	25	2.2	12	1.5	9.5	1.3	7.5	

(1) This parameter is ensured by design and/or characterization and is not tested in production.

### 8.6 Operating Characteristics

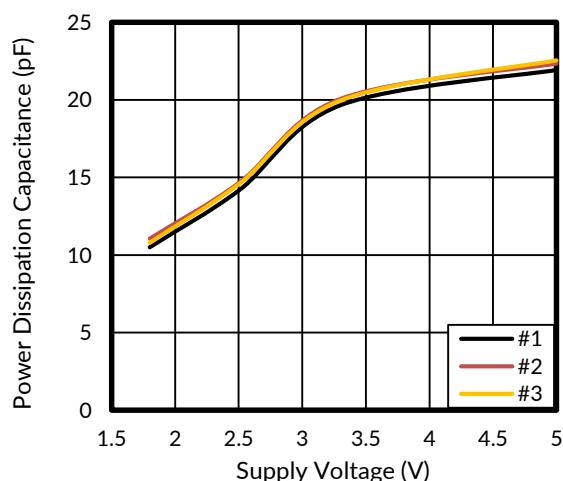
$T_A = +25^\circ\text{C}$

PARAMETER	TEST CONDITIONS	$V_{CC} = 1.8V$	$V_{CC} = 2.5V$	$V_{CC} = 3.3V$	$V_{CC} = 5V$	UNIT
		TYP	TYP	TYP	TYP	
$C_{pd}$ Power Dissipation Capacitance	$f = 10\text{ MHz}$	12	16	21	24	pF

### 8.7 Typical Characteristics

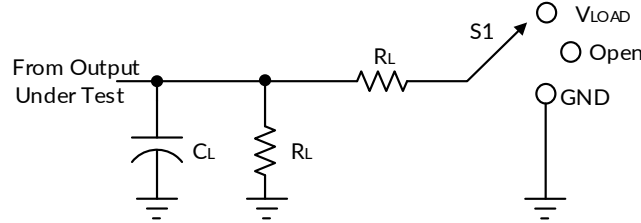
NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

At  $T_A = +25^\circ\text{C}$ , unless otherwise noted.



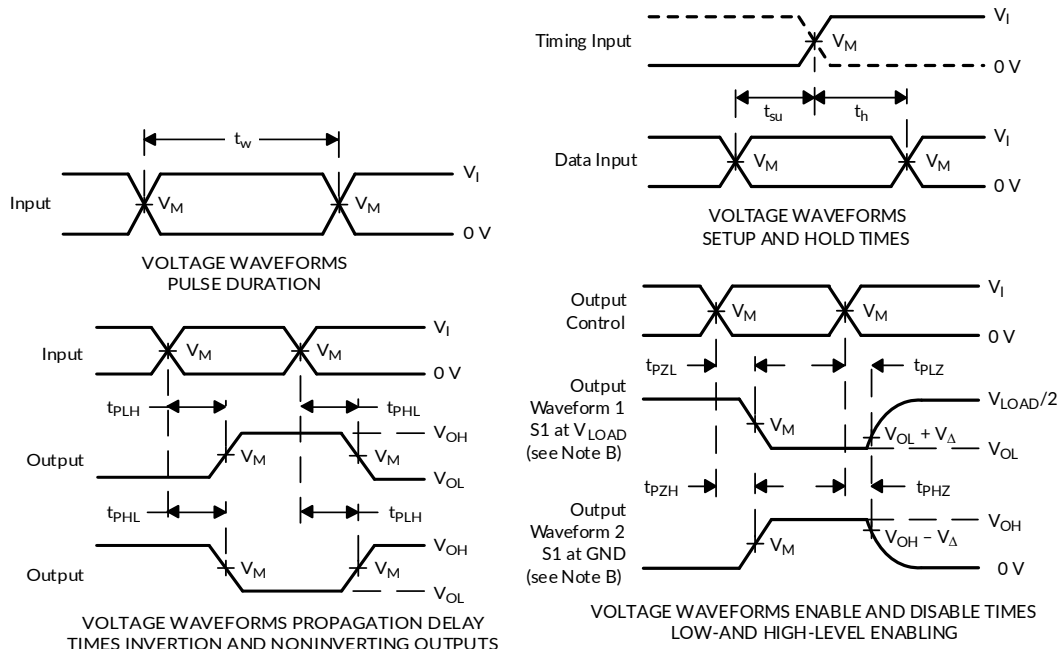
**Figure 1. Voltage vs Capacitance**

## 9 PARAMETER MEASUREMENT INFORMATION



TEST	S1
$t_{PLH}/t_{PHL}$	Open
$t_{PLZ}/t_{PZL}$	$V_{LOAD}$
$t_{PHZ}/t_{PZH}$	GND

$V_{CC}$	INPUTS		$V_M$	$V_{LOAD}$	CL		RL		$V_{\Delta}$
	$V_I$	$t_r/t_f$							
$1.8V \pm 0.15V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	15pF	30pF	1M $\Omega$	1k $\Omega$	0.15V
$2.5V \pm 0.2V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	15pF	30pF	1M $\Omega$	500 $\Omega$	0.15V
$3.3V \pm 0.3V$	3V	$\leq 2.5ns$	1.5V	6V	15pF	50pF	1M $\Omega$	500 $\Omega$	0.3V
$5V \pm 0.5V$	$V_{CC}$	$\leq 2.5ns$	$V_{CC}/2$	$2 \times V_{CC}$	15pF	50pF	1M $\Omega$	500 $\Omega$	0.3V



NOTES: A.  $C_L$  includes probe and jig capacitance.

B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10 \text{ MHz}$ ,  $Z_o = 50\Omega$ .

D. The outputs are measured one at a time, with one transition per measurement.

E.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .

F.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .

G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

H. All parameters and waveforms are not applicable to all devices.

**Figure 2. Load Circuit and Voltage Waveforms**

## 10 DETAILED DESCRIPTION

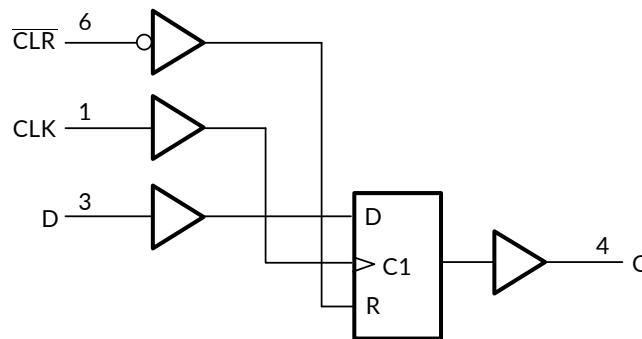
### 10.1 Overview

This single D-type flip-flop is designed for 1.65V to 5.5V  $V_{CC}$  operation.

The RS1G175 device has an asynchronous clear ( $\overline{CLR}$ ) input. When  $\overline{CLR}$  is high, data from the input pin (D) is transferred to the output pin (Q) on the clock's (CLK) rising edge. When  $\overline{CLR}$  is low, Q is forced into the low state, regardless of the clock edge or data on D.

This device is fully specified for partial-power-down applications using  $I_{off}$ . The  $I_{off}$  circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

### 10.2 Functional Block Diagram



### 10.3 Feature Description

The RS1G175 device has a wide operating  $V_{CC}$  range of 1.65 V to 5.5 V, which allows it to be used in a broad range of systems. The 5.5V I/Os allow down translation and also allow voltages at the inputs when  $V_{CC} = 0$ .

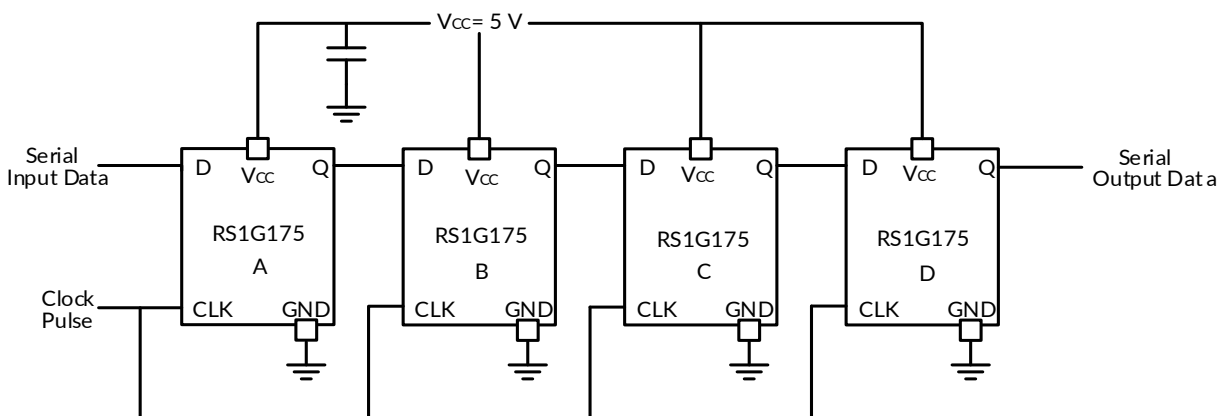
## 11 APPLICATION AND IMPLEMENTATION

Information in the following applications sections is not part of the Runic component specification, and Runic does not warrant its accuracy or completeness. Runic's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

### 11.1 Application Information

Multiple RS1G175 devices can be used in tandem to create a shift register of arbitrary length. In this example, we use four RS1G175 devices to form a 4-bit serial shift register. By connecting all CLK inputs to a common clock pulse and tying each output of one device to the next, we can store and load 4-bit values on demand. We demonstrate loading the 4-bit value 1101 into memory by setting Serial Input Data to each desired memory bit, and by sending a clock pulse for each bit, we sequentially move all stored bits from left to right (A → B → C → D)

### 11.2 Typical Application



**Figure 3. 4-Bit Serial Shift Register**

**Table 1. Stored Data Values**

Serial Input Data	Stored A	Stored B	Stored C	Stored D
1	0	0	0	0
0	1	0	0	0
1	0	1	0	0
1	1	0	1	0
0	1	1	0	1

### 11.3 Design Requirements

The RS1G175 device uses CMOS technology and has balanced output drive. Care must be taken to avoid bus contention because it can drive currents that would exceed maximum limits. The RS1G175 allows storing digital signals with a digital control signal. All input signals should remain as close as possible to either 0 V or V<sub>cc</sub> for optimal operation.

## 12 POWER SUPPLY RECOMMENDATIONS

The power supply pin should have a good bypass capacitor to prevent power disturbance. For devices with a single supply, a 0.1μF capacitor is recommended and if there are multiple V<sub>cc</sub> terminals then 0.01μF or 0.022μF capacitors are recommended for each power terminal. It is acceptable to parallel multiple bypass caps to reject different frequencies of noise. The 0.1μF and 1μF capacitors are commonly used in parallel. The bypass capacitor should be installed as close to the power terminal as possible.

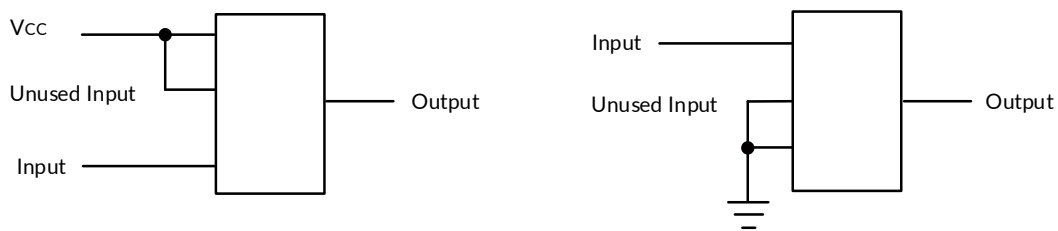
## 13 LAYOUT

### 13.1 Layout Guidelines

When using multiple bit logic devices, inputs should not float. In many cases, functions or parts of functions of digital logic devices are unused. Some examples are when only two inputs of a triple-input AND gate are used, or when only 3 of the 4-buffer gates are used. Such input pins should not be left unconnected because the undefined voltages at the outside connections result in undefined operational states.

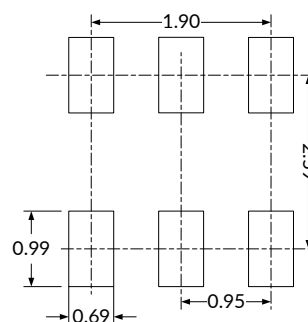
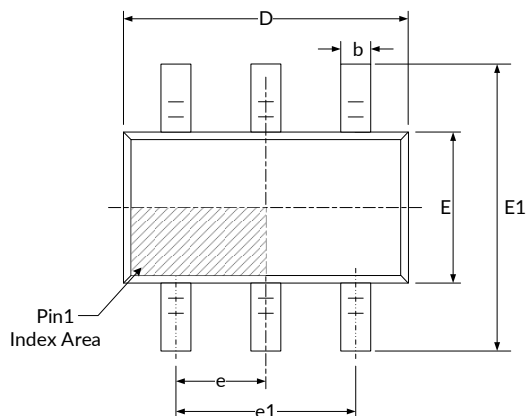
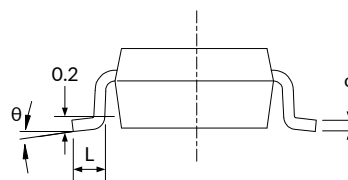
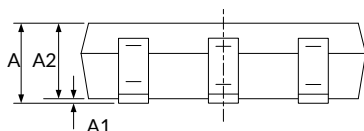
Specified in Figure 4 are rules that must be observed under all circumstances. All unused inputs of digital logic devices must be connected to a high or low bias to prevent them from floating. The logic level that should be applied to any particular unused input depends on the function of the device. Generally they will be tied to GND or  $V_{CC}$ , whichever makes more sense or is more convenient. It is acceptable to float outputs unless the part is a transceiver. If the transceiver has an output enable pin, it will disable the outputs section of the part when asserted. This will not disable the input section of the I/Os so they also cannot float when disabled.

### 13.2 Layout Example



**Figure 4. Layout Diagram**

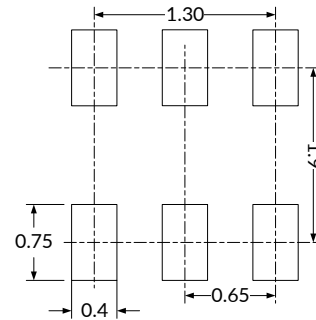
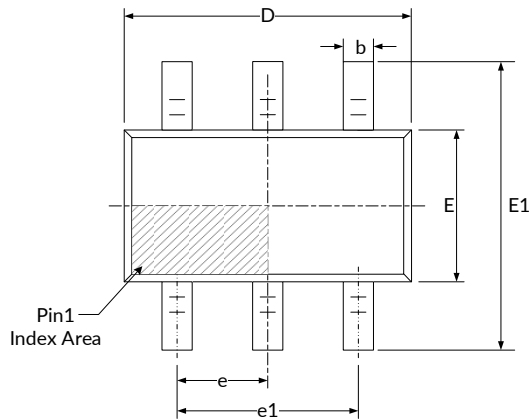
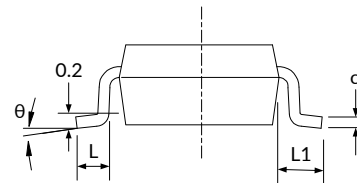
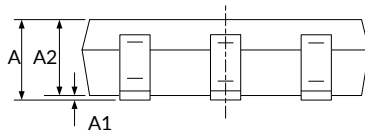
# 14 PACKAGE OUTLINE DIMENSIONS

**SOT23-6<sup>(3)</sup>**

**RECOMMENDED LAND PATTERN (Unit: mm)**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D <sup>(1)</sup>	2.820	3.020	0.111	0.119
E <sup>(1)</sup>	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC) <sup>(2)</sup>		0.037(BSC) <sup>(2)</sup>	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

**NOTE:**

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

**SC70-6 (3)**

**RECOMMENDED LAND PATTERN (Unit: mm)**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D <sup>(1)</sup>	2.000	2.200	0.079	0.087
E <sup>(1)</sup>	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650(BSC) <sup>(2)</sup>		0.026(BSC) <sup>(2)</sup>	
e1	1.300(BSC) <sup>(2)</sup>		0.051(BSC) <sup>(2)</sup>	
L	0.260	0.460	0.010	0.018
L1	0.525		0.021	
θ	0°	8°	0°	8°

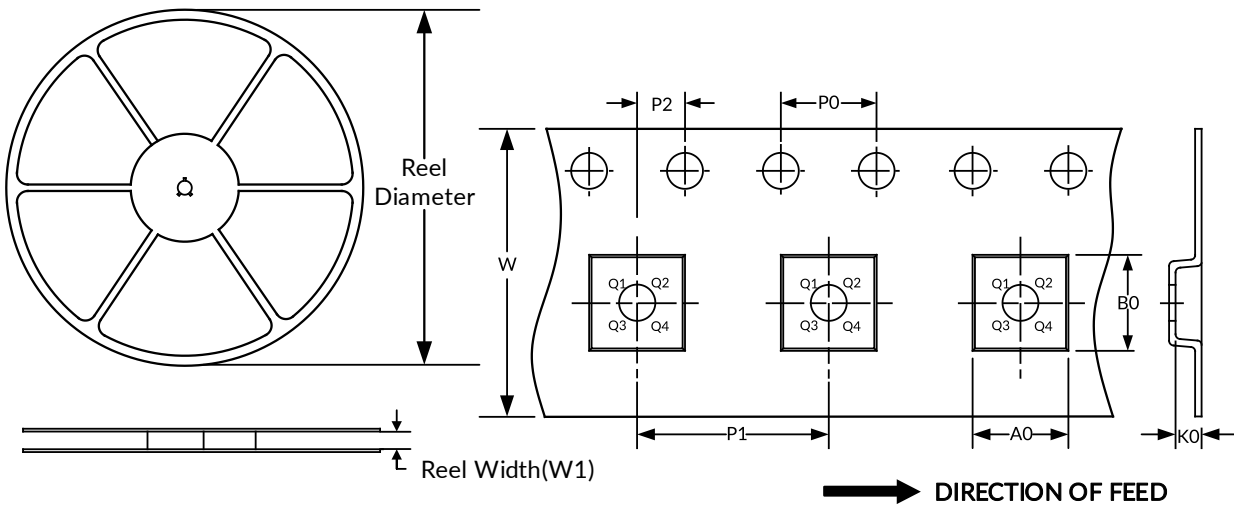
**NOTE:**

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

## 15 TAPE AND REEL INFORMATION

### REEL DIMENSIONS

### TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-6	7"	9.5	2.40	2.50	1.20	4.0	4.0	2.0	8.0	Q3
SOT23-6	7"	9.5	3.17	3.23	1.37	4.0	4.0	2.0	8.0	Q3

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.



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