

# RS4G157 Quad 2-Input Multiplexer

## 1 FEATURES

- **Operating Voltage Range: 1.65V to 5.5V**
- **Low Power Consumption: 10µA (Max)**
- **Operating Temperature Range: -40°C to +125°C**
- **Inputs Accept Voltage to 5.5V**
- **High Output Drive: ±24mA at V<sub>CC</sub>=3.0V**
- **I<sub>off</sub> Supports Live Insertion, Partial-Power Down Mode, and Back-Drive Protection**
- **Micro SIZE PACKAGES: TSSOP16, SOP16**

## 2 APPLICATIONS

- **Network Switch**
- **Telecom Infrastructure**
- **Servers**
- **I/O Expanders**
- **LED Displays**

## 3 DESCRIPTIONS

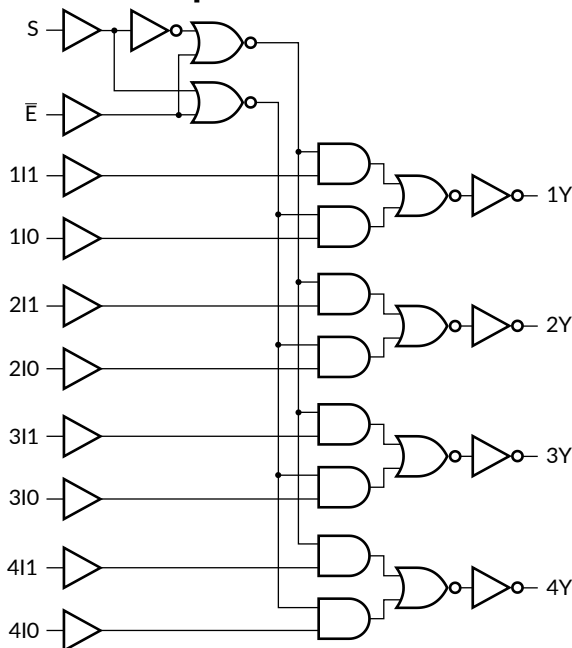
The RS4G157 quad 2-input multiplexer is designed for 1.65V to 5.5V V<sub>CC</sub> operation.

The RS4G157 selects data from two data inputs (I<sub>0</sub> and I<sub>1</sub>) under control of a common data select input (S). The enable input ( $\bar{E}$ ) is active Low. A HIGH on  $\bar{E}$  forces all the outputs (1Y to 4Y) LOW. The state of the common data select input determines the particular register from which the data comes. The output (Y) presents the selected data in the true (non-inverted) form.

The RS4G157 is fully specified for partial-power-down applications using I<sub>off</sub>. The I<sub>off</sub> circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

This device available in Green TSSOP16 and SOP16 packages. It operates over an ambient temperature range of -40°C to +125°C.

**Simplified Schematic**



**Device Information (1)**

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS4G157	TSSOP16	5.00mm×4.40mm
	SOP16	9.90mm×3.90mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

## Table of Contents

<b>1 FEATURES</b> .....	1
<b>2 APPLICATIONS</b> .....	1
<b>3 DESCRIPTIONS</b> .....	1
<b>4 Revision History</b> .....	3
<b>5 PACKAGE/ORDERING INFORMATION</b> <sup>(1)</sup> .....	4
<b>6 PIN CONFIGURATIONS</b> .....	5
6.1 PIN DESCRIPTION.....	5
6.2 FUNCTION TABLE .....	5
<b>7 SPECIFICATIONS</b> .....	6
7.1 Absolute Maximum Ratings <sup>(1)</sup> .....	6
7.2 ESD Ratings .....	6
<b>8 ELECTRICAL CHARACTERISTICS</b> .....	7
8.1 Recommended Operating Conditions.....	7
8.2 DC Characteristics .....	8
8.3 Switching Characteristics.....	9
8.4 Operating Characteristics .....	9
<b>9 Parameter Measurement Information</b> .....	10
<b>10 PACKAGE OUTLINE DIMENSIONS</b> .....	12
<b>11 TAPE AND REEL INFORMATION</b> .....	14

## 4 Revision History

Note: Page numbers for previous revisions may differ from page numbers in the current version.

Version	Change Date	Change Item
A.1	2023/11/21	Initial version completed
A.1.1	2024/02/29	Modify packaging naming

## 5 PACKAGE/ORDERING INFORMATION <sup>(1)</sup>

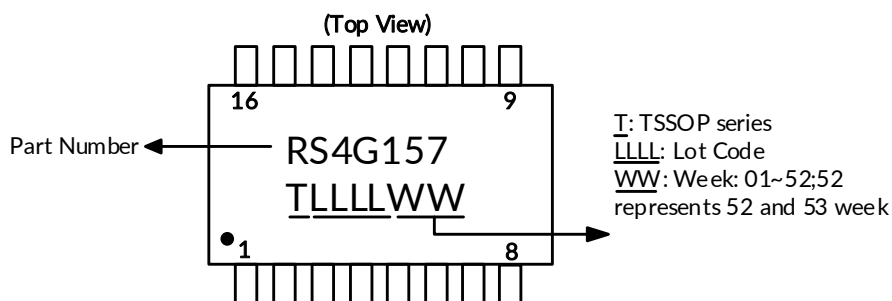
PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING <sup>(2)</sup>	MSL <sup>(3)</sup>	PACKAGE OPTION
RS4G157	RS4G157XTSS16	-40°C ~+125°C	TSSOP16	RS4G157	MSL3	Tape and Reel,4000
	RS4G157XS16	-40°C ~+125°C	SOP16	RS4G157	MSL3	Tape and Reel,4000

**NOTE:**

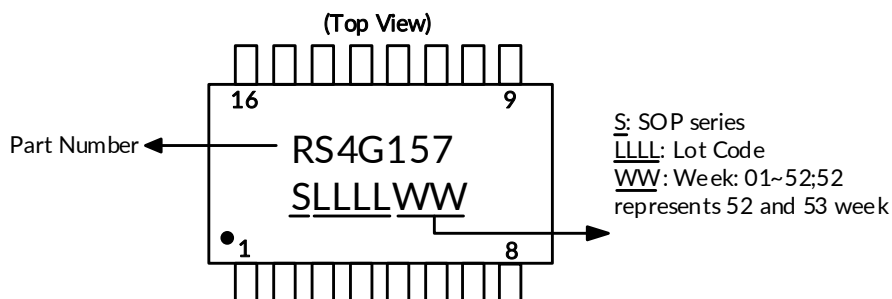
- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

### Marking Information

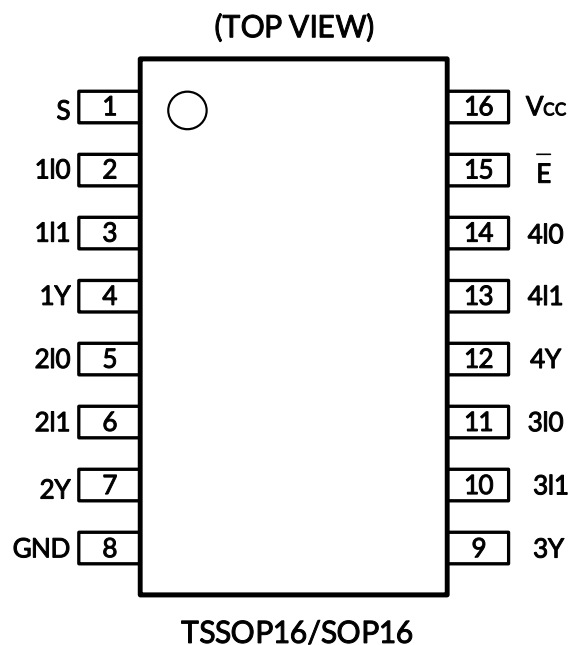
(1) TSSOP16



(2) SOP16



## 6 PIN CONFIGURATIONS



### 6.1 PIN DESCRIPTION

PIN	NAME	I/O TYPE <sup>(1)</sup>	FUNCTION
TSSOP16/SOP16			
1	S	I	Common data select input
2,5,11,14	1I0 to 4I0	I	Data inputs from source 0
3,6,10,13	1I1 to 4I1	I	Data inputs from source 1
4,7,9,12	1Y to 4Y	O	Multiplexer outputs
8	GND	G	Ground (0V)
15	$\bar{E}$	I	Enable input (active LOW)
16	V <sub>CC</sub>	P	Supply Voltage

(1) I=input, O=output, P=power, G=Ground.

### 6.2 FUNCTION TABLE

INPUTS				OUTPUT
$\bar{E}$	S	nI1	nI0	nY
H	X	X	X	L
L	L	X	L	L
L	L	X	H	H
L	H	L	X	L
L	H	H	X	H

(1) H=High Voltage Level  
L=Low Voltage Level  
X=Don't Care

## 7 SPECIFICATIONS

### 7.1 Absolute Maximum Ratings <sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted) <sup>(1) (2)</sup>

		MIN	MAX	UNIT
V <sub>CC</sub>	Supply voltage range	-0.5	6.5	V
V <sub>I</sub>	Input voltage range <sup>(2)</sup>	-0.5	6.5	V
V <sub>O</sub>	Voltage range applied to any output in the high-impedance or power-off state <sup>(2)</sup>	-0.5	6.5	V
V <sub>O</sub>	Voltage range applied to any output in the high or low state <sup>(2) (3)</sup>	-0.5	V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input clamp current	V <sub>I</sub> <0	-50	mA
I <sub>OK</sub>	Output clamp current	V <sub>O</sub> <0	-50	mA
I <sub>O</sub>	Continuous output current	V <sub>O</sub> = 0 to V <sub>CC</sub>	±50	mA
	Continuous current through V <sub>CC</sub> or GND		±100	mA
θ <sub>JA</sub>	Package thermal impedance <sup>(4)</sup>	TSSOP16	45	°C/W
		SOP16	150	
T <sub>J</sub>	Junction temperature <sup>(5)</sup>	-65	150	°C
T <sub>stg</sub>	Storage temperature	-65	150	°C

- (1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- (2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.
- (3) The value of V<sub>CC</sub> is provided in the Recommended Operating Conditions table.
- (4) The package thermal impedance is calculated in accordance with JESD-51.
- (5) The maximum power dissipation is a function of T<sub>J(MAX)</sub>, R<sub>θJA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any ambient temperature is P<sub>D</sub> = (T<sub>J(MAX)</sub> - T<sub>A</sub>) / R<sub>θJA</sub>. All numbers apply for packages soldered directly onto a PCB.

### 7.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

		VALUE	UNIT
V <sub>(ESD)</sub>	Electrostatic discharge	Human-body model (HBM), MIL-STD-883K METHOD 3015.9	±2000
		Charged-device model (CDM), ANSI/ESDA/JEDEC JS-002-2018	±1000
		Machine model (MM), JESD22-A115C (2010)	±200



#### ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## 8 ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range (TYP values are at  $T_A = +25^\circ\text{C}$ , Full= $-40^\circ\text{C}$  to  $125^\circ\text{C}$ , unless otherwise noted.) <sup>(1)</sup>

### 8.1 Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
Supply voltage	$V_{CC}$	Operating	1.65	5.5	V
High-level input voltage	$V_{IH}$	$V_{CC}=1.65\text{V to }1.95\text{V}$	$0.75 \times V_{CC}$		V
		$V_{CC}=2.3\text{V to }2.7\text{V}$	1.7		
		$V_{CC}=3\text{V to }3.6\text{V}$	2.2		
		$V_{CC}=4.5\text{V to }5.5\text{V}$	$0.7 \times V_{CC}$		
Low-level input voltage	$V_{IL}$	$V_{CC}=1.65\text{V to }1.95\text{V}$		$0.25 \times V_{CC}$	V
		$V_{CC}=2.3\text{V to }2.7\text{V}$		0.7	
		$V_{CC}=3\text{V to }3.6\text{V}$		0.8	
		$V_{CC}=4.5\text{V to }5.5\text{V}$		$0.3 \times V_{CC}$	
Input voltage	$V_I$		0	5.5	V
Output voltage	$V_O$		0	$V_{CC}$	V
High-level output current	$I_{OH}$	$V_{CC}=1.65\text{V}$		-4	mA
		$V_{CC}=2.3\text{V}$		-8	
		$V_{CC}=3\text{V}$		-16	
		$V_{CC}=4.5\text{V}$		-32	
Low-level output current	$I_{OL}$	$V_{CC}=1.65\text{V}$		4	mA
		$V_{CC}=2.3\text{V}$		8	
		$V_{CC}=3\text{V}$		16	
		$V_{CC}=4.5\text{V}$		32	
Input transition rise or fall	$\Delta t / \Delta v$	$V_{CC}=1.8\text{V} \pm 0.15\text{V}, 2.5\text{V} \pm 0.2\text{V}$		20	ns/V
		$V_{CC}=3.3\text{V} \pm 0.3\text{V}$		10	
		$V_{CC}=5\text{V} \pm 0.5\text{V}$		10	
Operating temperature	$T_A$		-40	125	$^\circ\text{C}$

(1) All unused inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation.

**8.2 DC Characteristics**

PARAMETER		TEST CONDITIONS	V <sub>CC</sub>	TEMP	MIN <sup>(2)</sup>	TYP <sup>(3)</sup>	MAX <sup>(2)</sup>	UNIT
V <sub>OH</sub>		I <sub>OH</sub> = -100μA	1.65V to 5.5V	25°C	V <sub>CC</sub> -0.1			V
		I <sub>OH</sub> = -4mA	1.65V		1.2			
		I <sub>OH</sub> = -8mA	2.3V		1.9			
		I <sub>OH</sub> = -16mA	3V		2.4			
		I <sub>OH</sub> = -24mA			2.3			
		I <sub>OH</sub> = -32mA	4.5V		3.8			
V <sub>OL</sub>		I <sub>OL</sub> = 100μA	1.65V to 5.5V	25°C			0.1	V
		I <sub>OL</sub> = 4mA	1.65V				0.45	
		I <sub>OL</sub> = 8mA	2.3V				0.3	
		I <sub>OL</sub> = 16mA	3V				0.4	
		I <sub>OL</sub> = 24mA					0.55	
		I <sub>OL</sub> = 32mA	4.5V				0.55	
I <sub>i</sub>	All inputs	V <sub>I</sub> =5.5V or GND	0V to 5.5V	25°C		±0.1	±1	μA
				Full			±5	
I <sub>off</sub>		V <sub>I</sub> or V <sub>O</sub> =5.5V	0	25°C		±0.1	±1	μA
				Full			±10	
I <sub>CC</sub>		V <sub>I</sub> =5.5V or GND, I <sub>O</sub> =0	1.65V to 5.5V	25°C		0.1	1	μA
				Full			10	
ΔI <sub>CC</sub>		One input at V <sub>CC</sub> -0.6V, Other inputs at V <sub>CC</sub> or GND	3V to 5.5V	Full			500	μA
C <sub>i</sub> (Input Capacitance)		nI0, nI1, f=1MHZ	3.3V	25°C		2		pF
		S, $\bar{E}$ , f=1MHZ				8		

(1) All unused inputs of the device must be held at V<sub>CC</sub> or GND to ensure proper device operation.

(2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.



### 8.3 Switching Characteristics

over recommended operating free-air temperature range ( $T_A = +25^\circ\text{C}$ , unless otherwise noted) <sup>(1)</sup>

PARAMETER	SYMBOL	TEST CONDITIONS		TEMP	MIN	TYP	MAX	UNIT
Propagation Delay	$t_{pd}$	nI0, nI1 to nY	$V_{CC}=1.8V\pm0.15V$	FULL	8	16	30	ns
			$V_{CC}=2.5V\pm0.2V$		3.4	9	17	
			$V_{CC}=3.3V\pm0.3V$		2.6	7	12	
			$V_{CC}=5V\pm0.5V$		1.3	5	9.2	
		S to nY	$V_{CC}=1.8V\pm0.15V$	FULL	11	21	39	ns
			$V_{CC}=2.5V\pm0.2V$		4.2	11	21	
			$V_{CC}=3.3V\pm0.3V$		2.6	7	12	
			$V_{CC}=5V\pm0.5V$		1.6	6	11	
		$\bar{E}$ to nY	$V_{CC}=1.8V\pm0.15V$	FULL	11	21	39	ns
			$V_{CC}=2.5V\pm0.2V$		3.4	9	17	
			$V_{CC}=3.3V\pm0.3V$		3	8	15	
			$V_{CC}=5V\pm0.5V$		1.6	6	11	
Transition Time	$t_t$	nY	$V_{CC}=1.8V\pm0.15V$	25°C		7		ns
			$V_{CC}=2.5V\pm0.2V$			4		
			$V_{CC}=3.3V\pm0.3V$			5		
			$V_{CC}=5V\pm0.5V$			3		

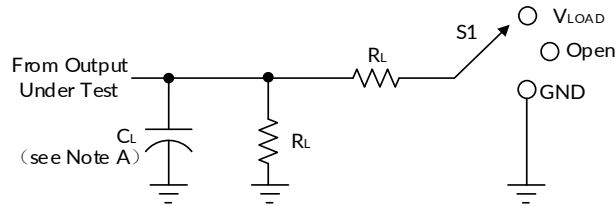
(1) This parameter is ensured by design and/or characterization and is not tested in production.

### 8.4 Operating Characteristics

$T_A = +25^\circ\text{C}$

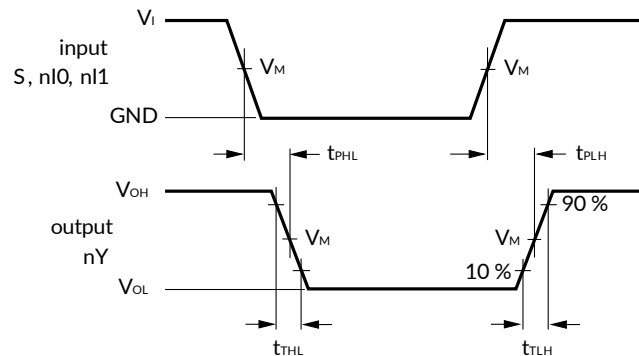
PARAMETER	TEST CONDITIONS	$V_{CC} = 1.8V$	$V_{CC} = 2.5V$	$V_{CC} = 3.3V$	$V_{CC} = 5V$	UNIT
		TYP	TYP	TYP	TYP	
$C_{pd}$ Power dissipation capacitance	$f = 10 \text{ MHz}$	22	25	32	40	pF

## 9 Parameter Measurement Information



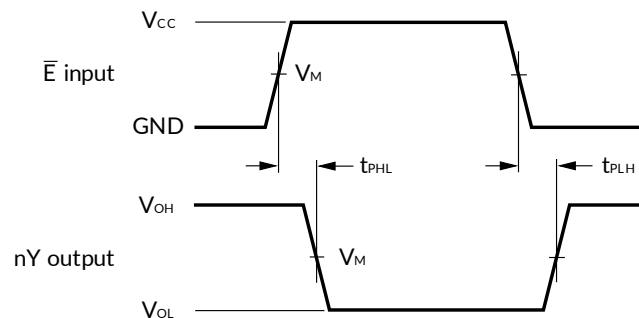
TEST	S1
t <sub>PLH</sub> /t <sub>PHL</sub>	Open
t <sub>PLZ</sub> /t <sub>PZL</sub>	V <sub>LOAD</sub>
t <sub>PHZ</sub> /t <sub>PZH</sub>	GND

V <sub>CC</sub>	INPUTS		V <sub>M</sub>	V <sub>LOAD</sub>	C <sub>L</sub>	R <sub>L</sub>	V <sub>Δ</sub>
	V <sub>I</sub>	t <sub>r</sub> /t <sub>f</sub>					
1.8V±0.15V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	2 x V <sub>CC</sub>	30pF	1kΩ	0.15V
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	2 x V <sub>CC</sub>	30pF	500Ω	0.15V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	V <sub>CC</sub>	≤2.5ns	V <sub>CC</sub> /2	2 x V <sub>CC</sub>	50pF	500Ω	0.3V



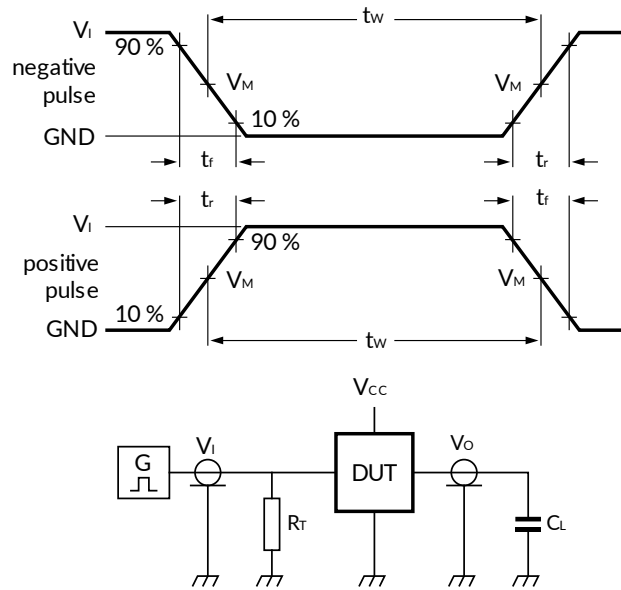
V<sub>OL</sub> and V<sub>OH</sub> are typical voltage output levels that occur with the output load.

**Figure 1. Propagation delay input (nI0, nI1, S) to output (nYn)**



V<sub>OL</sub> and V<sub>OH</sub> are typical voltage output levels that occur with the output load.

**Figure 2. Propagation delay input ( $\bar{E}$ ) to output (nY)**



Definitions test circuit:

$R_T$  = Termination resistance should be equal to output impedance  $Z_o$  of the pulse generator.

$C_L$  = Load capacitance including jig and probe capacitance.

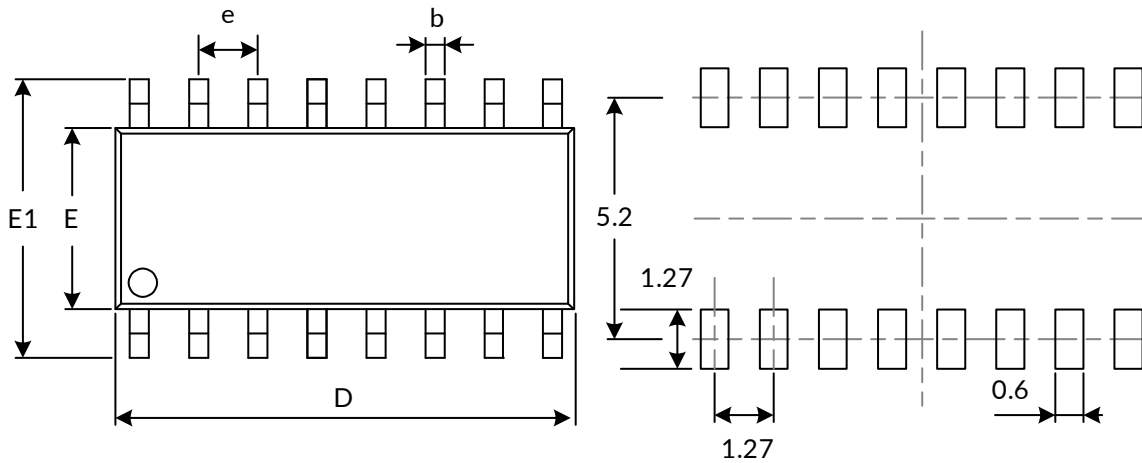
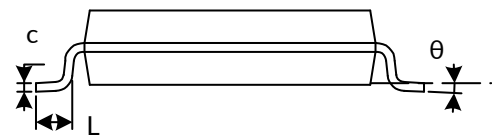
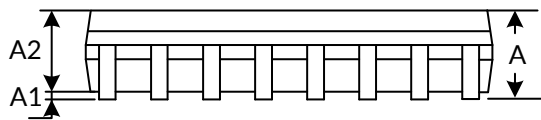
$R_L$  = Load resistance.

$S1$  = Test selection switch.

**Figure 3. Test circuit for measuring switching times**

# 10 PACKAGE OUTLINE DIMENSIONS

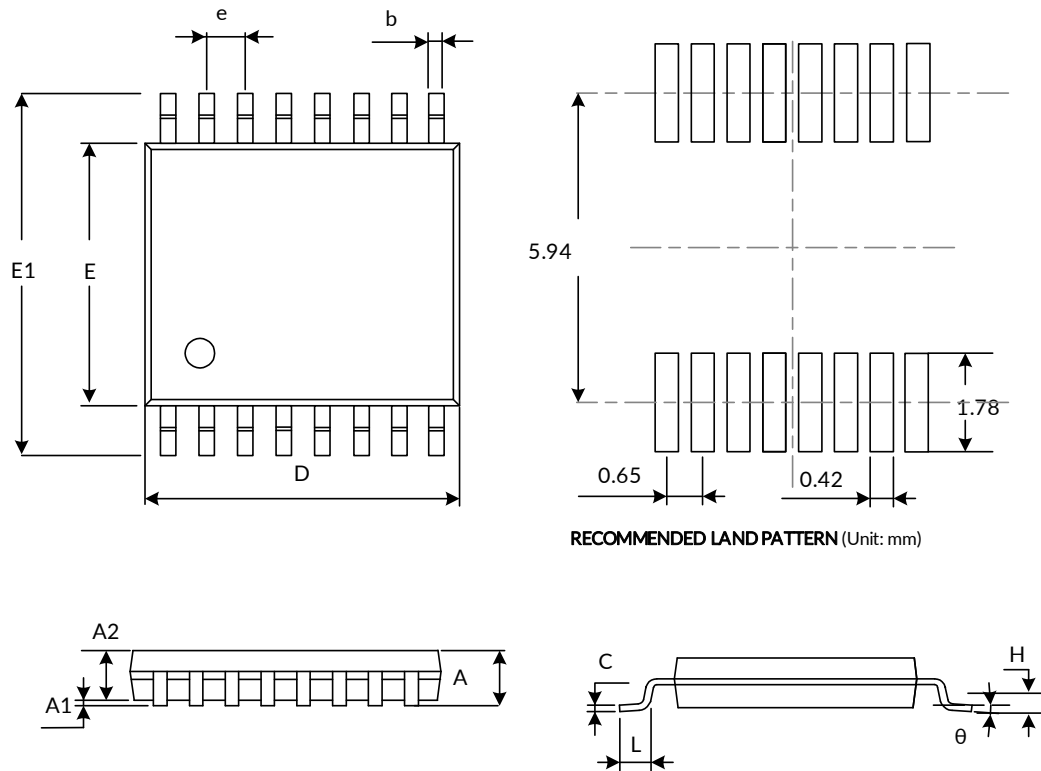
## SOP16<sup>(3)</sup>


**RECOMMENDED LAND PATTERN (Unit: mm)**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>		1.750		0.069
A1	0.100	0.225	0.004	0.009
A2	1.300	1.500	0.051	0.059
b	0.390	0.470	0.015	0.019
c	0.200	0.240	0.007	0.010
D <sup>(1)</sup>	9.800	10.00	0.386	0.394
E <sup>(1)</sup>	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC) <sup>(2)</sup>		0.050 (BSC) <sup>(2)</sup>	
L	0.500	0.800	0.020	0.032
θ	0°	8°	0°	8°

**NOTE:**

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

**TSSOP16 (3)**

**RECOMMENDED LAND PATTERN (Unit: mm)**

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.900	1.050	0.035	0.041
b	0.200	0.280	0.007	0.011
c	0.130	0.170	0.005	0.007
D <sup>(1)</sup>	4.900	5.100	0.193	0.201
E <sup>(1)</sup>	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.260
e	0.650(BSC) <sup>(2)</sup>		0.026(BSC) <sup>(2)</sup>	
L	0.450	0.750	0.017	0.030
H	0.250 TYP		0.010 TYP	
$\theta$	0°	8°	0°	8°

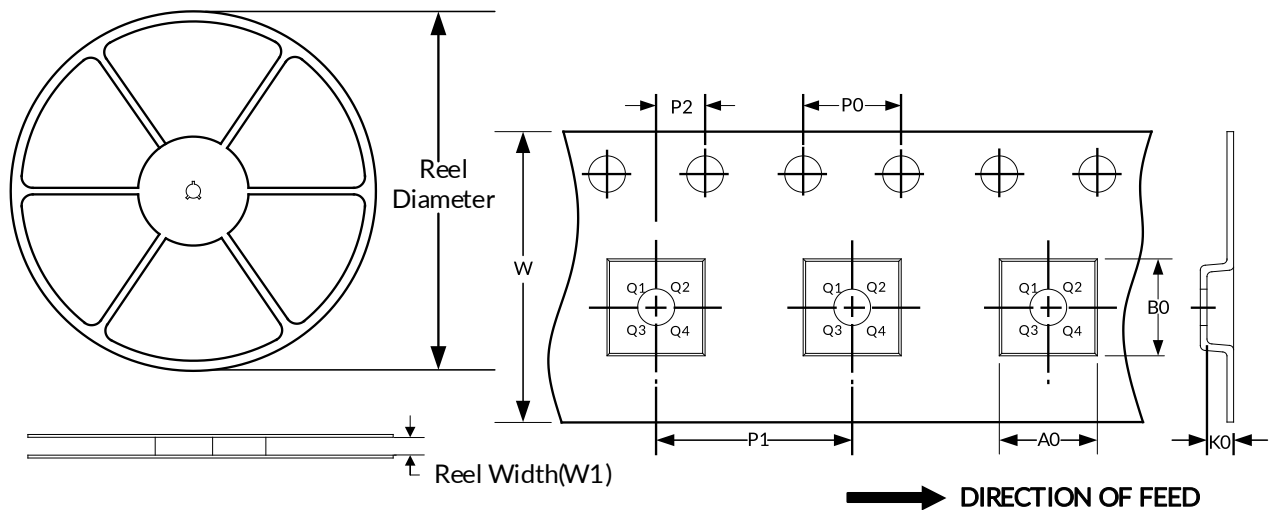
**NOTE:**

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

# 11 TAPE AND REEL INFORMATION

## REEL DIMENSIONS

## TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width(mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOP16	13"	16.4	6.50	10.30	2.10	4.0	8.0	2.0	16.0	Q1
TSSOP16	13"	12.4	6.90	5.60	1.20	4.0	8.0	2.0	12.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

## **IMPORTANT NOTICE AND DISCLAIMER**

Jiangsu Runic Technology Co., Ltd. will accurately and reliably provide technical and reliability data (including data sheets), design resources (including reference designs), application or other design advice, WEB tools, safety information and other resources, without warranty of any defect, and will not make any express or implied warranty, including but not limited to the warranty of merchantability Implied warranty that it is suitable for a specific purpose or does not infringe the intellectual property rights of any third party.

These resources are intended for skilled developers designing with Runic products You will be solely responsible for: (1) Selecting the appropriate products for your application; (2) Designing, validating and testing your application; (3) Ensuring your application meets applicable standards and any other safety, security or other requirements; (4) Runic and the Runic logo are registered trademarks of Runic Incorporated. All trademarks are the property of their respective owners; (5) For change details, review the revision history included in any revised document. The resources are subject to change without notice. Our company will not be liable for the use of this product and the infringement of patents or third-party intellectual property rights due to its use.