4.5Ω Quad SPDT Analog Switch 4-Channel 2:1 Multiplexer – Demultiplexer With Two Controls

1 FEATURES

- **High Bandwidth: 300MHz**
- **High Speed: Typically 30ns**
- **Supply Range: +1.8V to +5.5V**
- **Low ON-State Resistance, 4.5Ω(TYP)**
- **Break-Before-Make Switching**
- **Rail-to-Rail Operation**
- **TTL/CMOS Compatible**
- **Extended Industrial Temperature Range: -40°C to +125°C**

2 APPLICATIONS

- **Video Switching**
- **Relay Replacements**
- **USB Switching**
- **Battery-Operated Equipment**
- **Cell Phones**

3 DESCRIPTIONS

The RS2299 is a bidirectional 4-channel single-pole double-throw (SPDT) analog switch with two control inputs, which is designed to operate from 1.8V to 5.5V. This device is also known as a 2 channel double-pole double-throw (DPDT) configuration.

The RS2299 device can handle both analog and digital signals. It features high-bandwidth (300MHz) and low on-resistance (4.5Ω TYP).

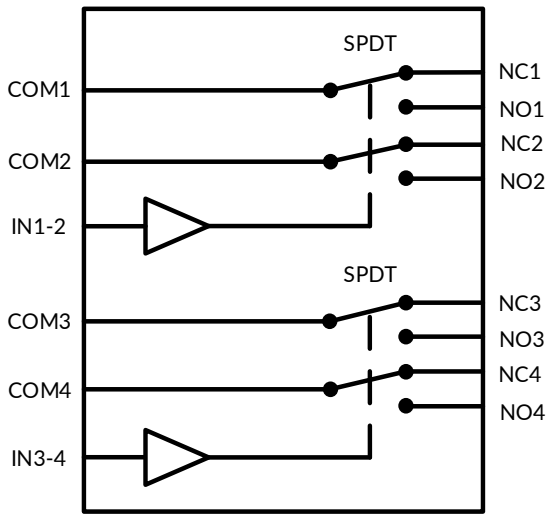
Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog-to-digital and digital-to-analog conversion systems.

Device Information (1)

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS2299	QFN3X3-16	3.00mm×3.00mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

4 FUNCTIONAL BLOCK DIAGRAM



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5 REVISION HISTORY

Note: Page numbers for previous revisions may different from page numbers in the current version.

VERSION	Change Date	Change Item
C.4.1	2024/03/08	1. Added the TAPE AND REEL INFORMATION 2. Change Thermal Information on Page 2@RevC.4 3. Modify packaging naming
C.5	2024/05/08	1. Add MSL on Page 5@RevC.4.1 2. Add Package thermal impedance on Page 4@RevC.4.1 3. Update PACKAGE note

6 PACKAGE/ORDERING INFORMATION (1)

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING (2)	MSL (3)	PACKAGE OPTION
RS2299	RS2299XTQC16	-40°C ~125°C	QFN3X3-16	RS2299	MSL3	Tape and Reel, 5000

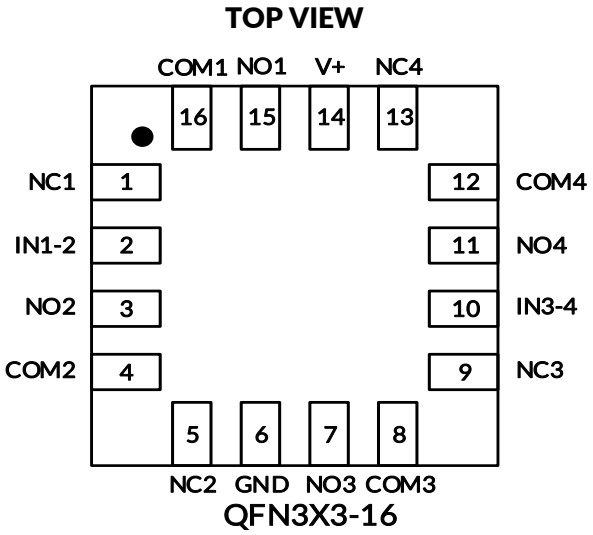
NOTE:

(1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.

(2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.

(3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

7 PIN CONFIGURATIONS



7.1 Pin Description

NAME	PIN	FUNCTION
	QFN3X3-16	
V+	14	Power Supply
GND	6	Ground
IN1-2	2	Digital Control Pin
IN3-4	10	Digital Control Pin
COMx	16, 4, 8, 12	Common Terminal
NOx	15, 3, 7, 11	Normally-Open Terminal
NCx	1, 5, 9, 13	Normally-Closed Terminal

7.2 Function Table

IN1-2	NO1 and NO2	NC1 and NC2
0	OFF	ON
1	ON	OFF

IN3-4	NO3 and NO4	NC3 and NC4
0	OFF	ON
1	ON	OFF

8 SPECIFICATIONS

8.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) (1)

SYMBOL	PARAMETER		MIN	MAX	UNIT
V_+	Supply Voltage		-0.3	6.0	V
V_{IN}	Input Voltage (2)		-0.3	6.0	
	Analog, Digital Voltage Range		-0.3	V_{CC}+0.3	
	Continuous Current NO, NC, or COM		-300	300	mA
I_{PEAK}	Peak Current NO, NC, or COM		-500	500	
θ_{JA}	Package thermal impedance (3)	QFN3X3-16		70	°C/W
T_J	Junction Temperature (4)		-40	150	°C
T_{stg}	Storage temperature		-65	150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.
(2) All voltages are with respect to ground, unless otherwise specified.
(3) The package thermal impedance is calculated in accordance with JESD-51.
(4) The maximum power dissipation is a function of $T_{J(MAX)}$, $R_{\theta JA}$, and T_A. The maximum allowable power dissipation at any ambient temperature is $P_D = (T_{J(MAX)} - T_A) / R_{\theta JA}$. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			VALUE	UNIT
$V_{(ESD)}$	Electrostatic discharge	Human-Body Model (HBM)	±1000	V
		Machine Model (MM)	±100	V

ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

8.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted)

SYMBOL	PARAMETER	MIN	MAX	UNIT
V_{CC}	Supply Voltage	1.8	5.5	V
T_A	Operating temperature	-40	+125	°C

8.4 Electrical Characteristics

V+ = 5.0 V, T_A= -40°C to 125°C (unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS	V_+	T_A	MIN(2)	TYP(3)	MAX(2)	UNIT
ANALOG SWITCH								
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}			FULL	0		V+	V
On-Resistance	R_{ON}	V_{NO} or V_{NC} = V+/2, I_{COM} = -10mA, Switch ON, See Figure 4	5V	+25°C		4.5	8	Ω
				FULL			8.5	Ω
			3.3V	+25°C		7	10	Ω
				FULL			10.5	Ω
On-Resistance Match Between Channels	ΔR_{ON}	V_{NO} or V_{NC} = V+/2, I_{COM} = -10mA, Switch ON, See Figure 4	5V	+25°C		0.15	0.3	Ω
				FULL			0.4	Ω
			3.3V	+25°C		0.15	0.3	Ω
				FULL			0.4	Ω
On-Resistance Flatness	$R_{FLAT(ON)}$	0 ≤ (V_{NO} or V_{NC}) ≤V+/2, I_{COM} = -10mA, Switch ON, See Figure 4	5V	+25°C		2	3	Ω
				FULL			3.3	Ω
			3.3V	+25°C		3	4	Ω
				FULL			4.3	Ω
NC, NO OFF Leakage Current	$I_{NC(OFF)}$, $I_{NO(OFF)}$	V_{NO} or V_{NC} = 0.3V, V+/2 V_{COM} = V+/2, 0.3V See Figure 5	1.8 to 5.5V	FULL			1	μA
NC, NO, COM ON Leakage Current	$I_{NC(ON)}$, $I_{NO(ON)}$, $I_{COM(ON)}$	V_{NO} or V_{NC} = 0.3V, Open V_{COM} = Open, 0.3V See Figure 6	1.8 to 5.5V	FULL			1	μA
DIGITAL CONTROL INPUTS (1)								
Input High Voltage	V_{INH}		5V	FULL	1.5			V
			3.3V	FULL	1.3			V
Input Low Voltage	V_{INL}		5V	FULL			0.6	V
			3.3V	FULL			0.5	V
Input Leakage Current	I_{IN}	V_{IN} = V_{IO} or 0	1.8 to 5.5V	FULL			1	μA

(1) All unused digital inputs of the device must be held at V_{IO} or GND to ensure proper device operation.
(2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.
(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

Electrical Characteristics (continued)

V+ = 5.0 V, T_A= -40°C to 125°C (unless otherwise noted)

PARAMETER	SYMBOL	CONDITIONS		V_+	T_A	MIN	TYP	MAX	UNIT
DYNAMIC CHARACTERISTICS									
Turn-On Time	t_{ON}	V_{COM} = V+, R_L = 300Ω, C_L = 35pF, See Figure 8		5V	+25°C		30		ns
				3.3V			40		
Turn-Off Time	t_{OFF}	V_{COM} = V+, R_L = 300Ω, C_L = 35pF, See Figure 8		5V	+25°C		25		ns
				3.3V			30		
Break-Before-Make Time Delay	t_{BBM}	V_{NO1} = V_{NC1} = V_{NO2} = V_{NC2} = 3V, R_L = 300Ω, C_L = 35pF, See Figure 9		5V	+25°C		5		ns
				3.3V			8		
Off Isolation	O_{ISO}	R_L = 50Ω, Switch OFF, See Figure 11	f = 10MHz		+25°C		-52		dB
			f = 1MHz		+25°C		-71		dB
-3dB Bandwidth	BW	Switch ON, R_L = 50Ω, See Figure 10			+25°C		300		MHz
NC, NO OFF Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$	V_{NC} or V_{NO}=V+/2 or GND, Switch OFF See Figure 7			+25°C		5		pF
NC, NO, COM ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$	V_{NC} or V_{NO}=V+/2 or GND, Switch ON See Figure 7			+25°C		15		pF
POWER REQUIREMENTS									
Power Supply Range	V_+				FULL	1.8		5.5	V
Power Supply Current	I_+	V_{IN} = GND		5.5V	FULL			1	μA
		V_{IN} = V_+		5.5V	FULL			1	μA

8.5 Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

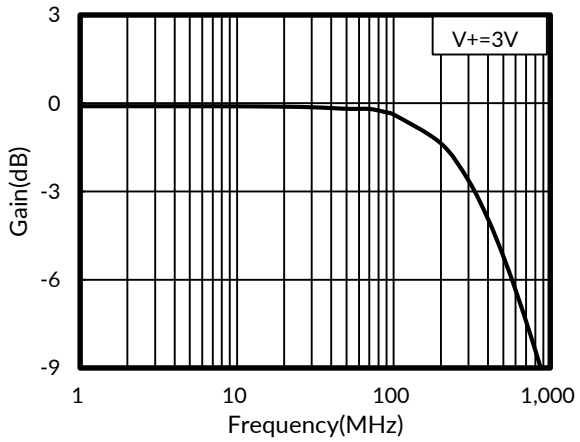


Figure 1. Bandwidth vs Frequency

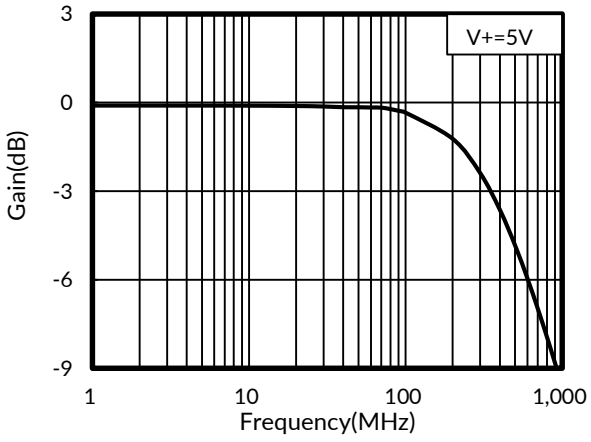


Figure 2. Bandwidth vs Frequency

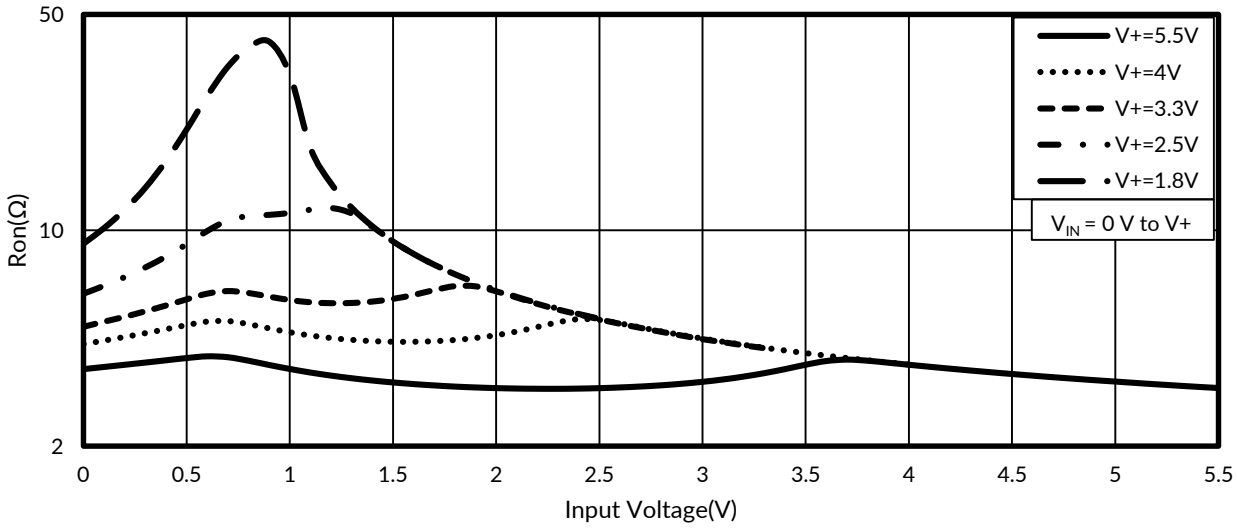


Figure 3. Typical Ron as a Function of Input Voltage

9 PARAMETER MEASUREMENT INFORMATION

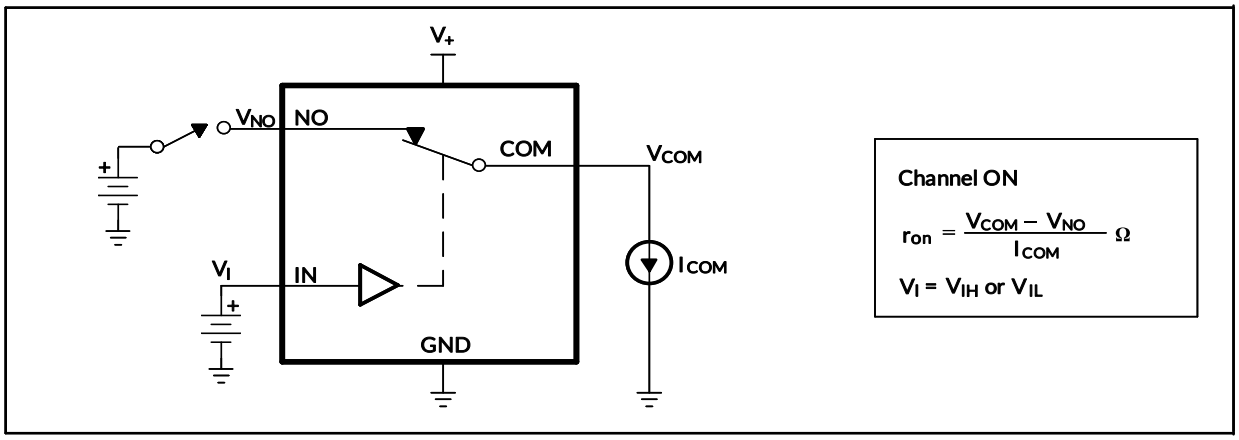


Figure 4. ON-State Resistance (R_{on})

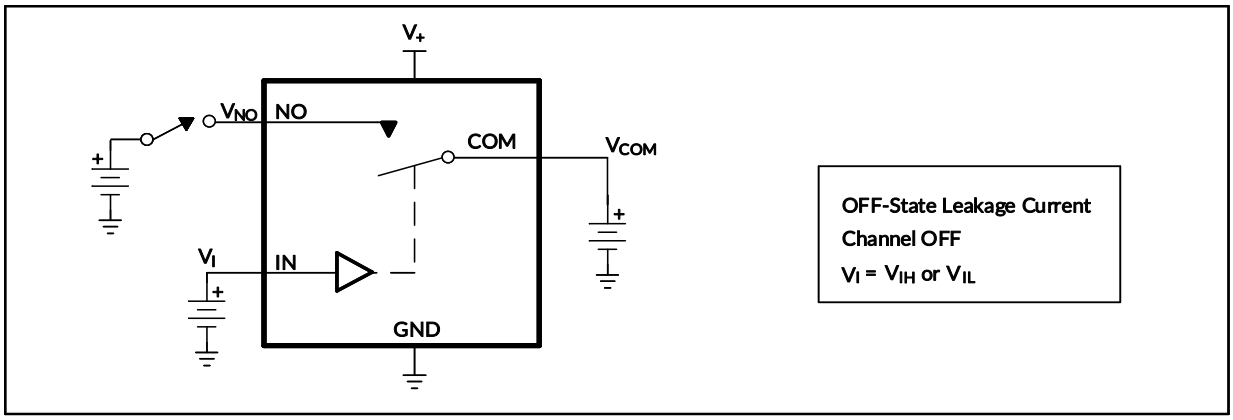


Figure 5. OFF-State Leakage Current ($I_{COM\ (OFF)}$, $I_{NO\ (OFF)}$)

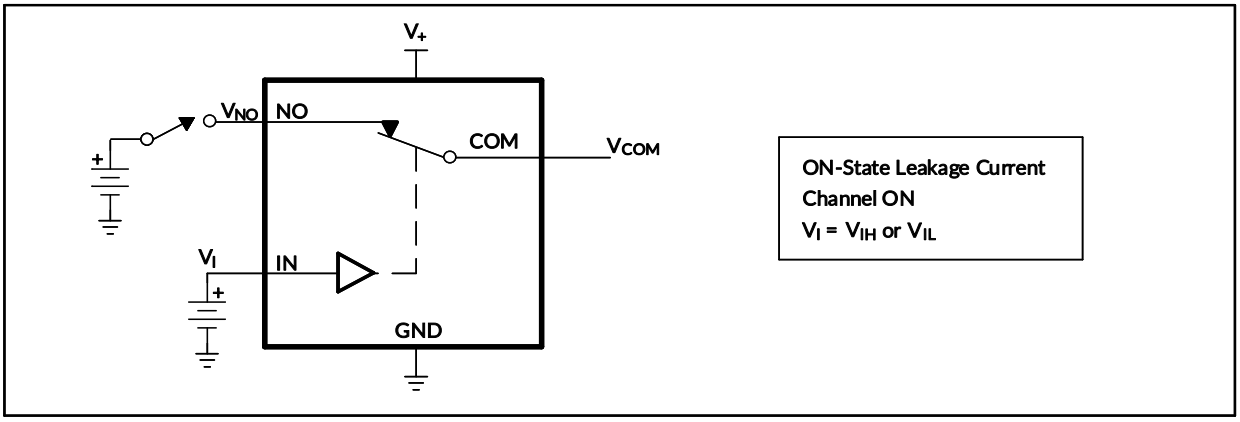


Figure 6. ON-State Leakage Current ($I_{COM\ (ON)}$, $I_{NO\ (ON)}$)

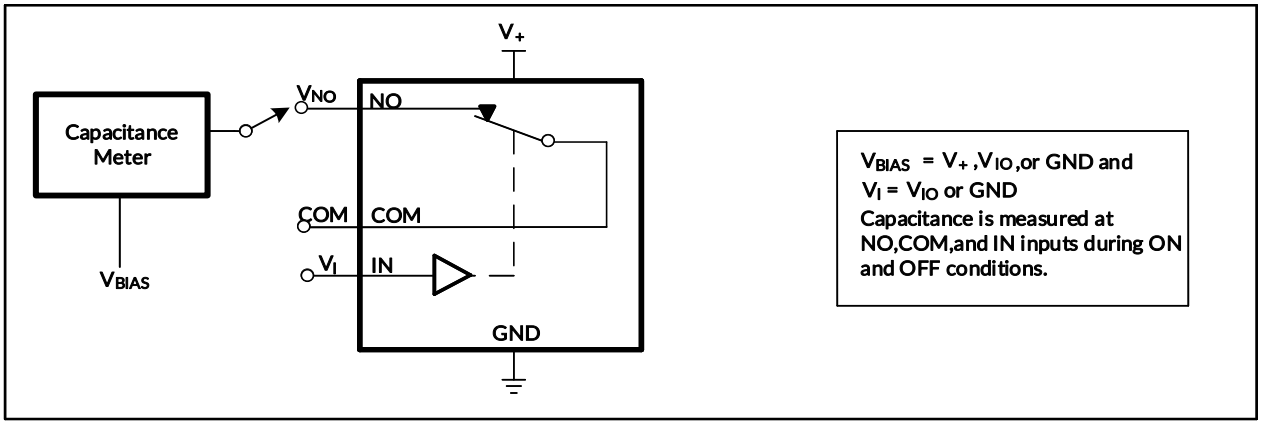


Figure 7. Capacitance (C_I, $C_{COM\ (OFF)}$, $C_{COM\ (ON)}$, $C_{NO\ (OFF)}$, $C_{NO\ (ON)}$)

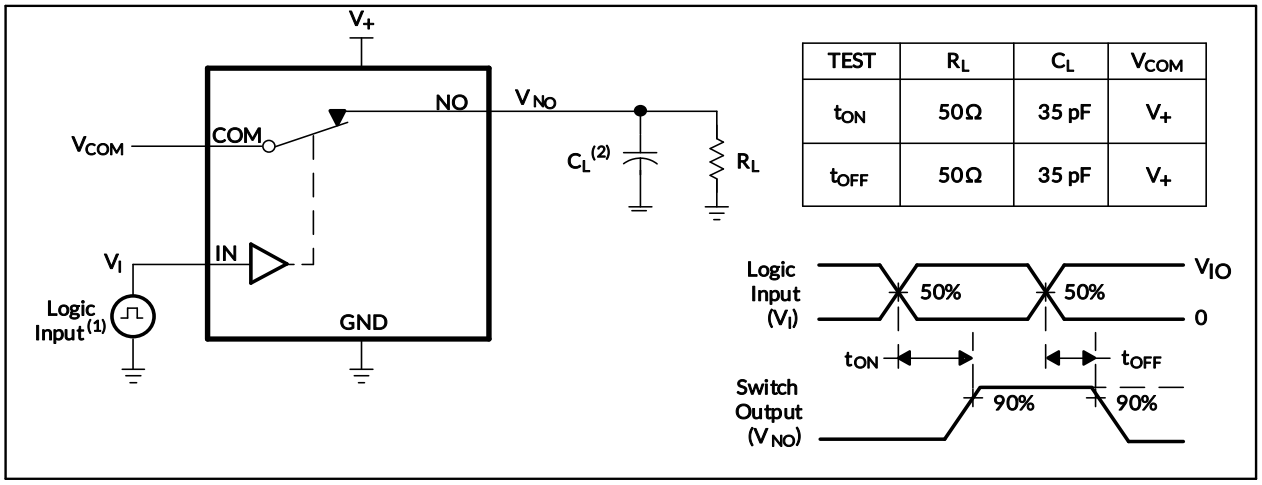


Figure 8. Turn-On (t_{ON}) and Turn-Off Time (t_{OFF})

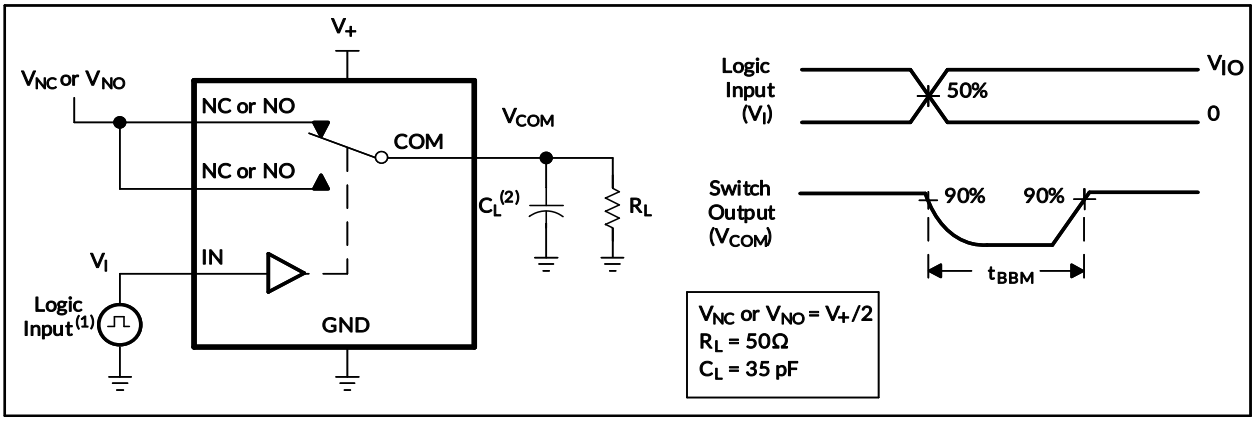


Figure 9. Break-Before-Make Time (t_{BBM})

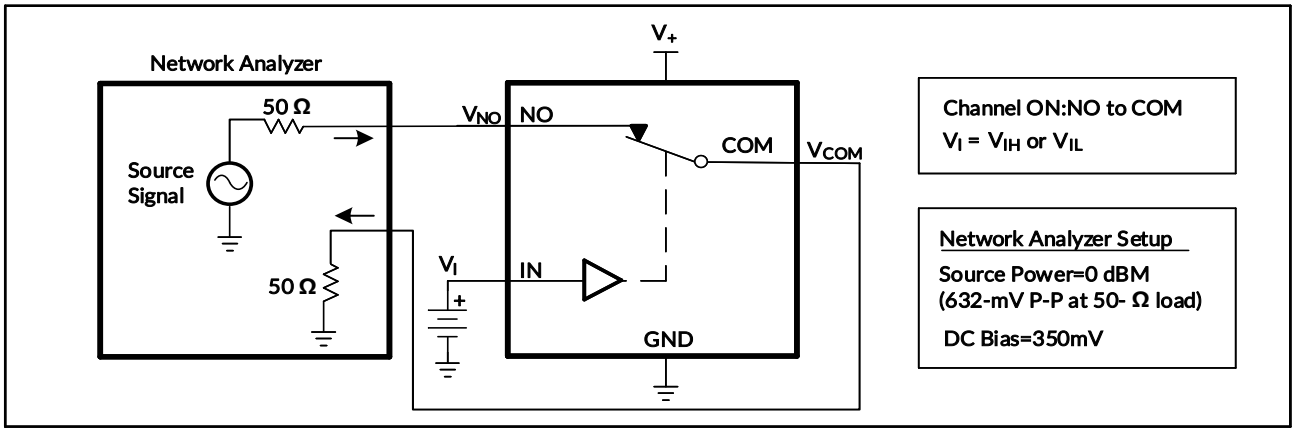


Figure 10. Bandwidth (BW)

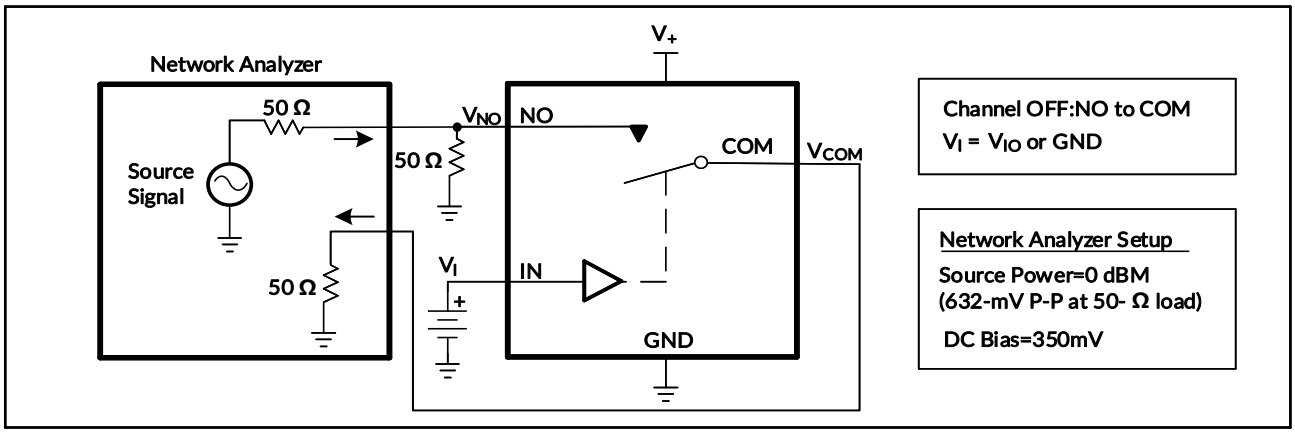


Figure 11. OFF Isolation (O_{ISO})

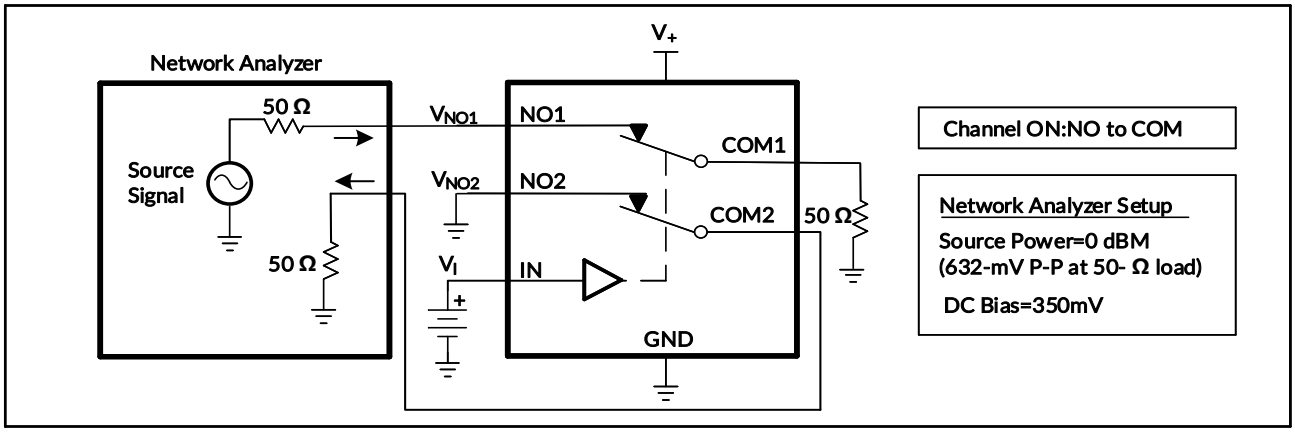


Figure 12. Crosstalk (X_{TALK})

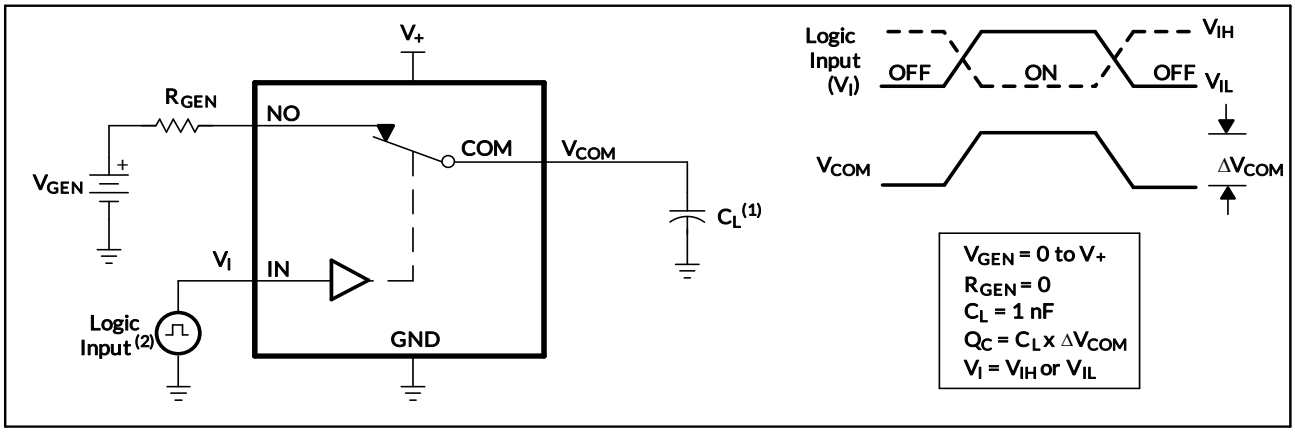


Figure 13. Charge Injection (Q_C)

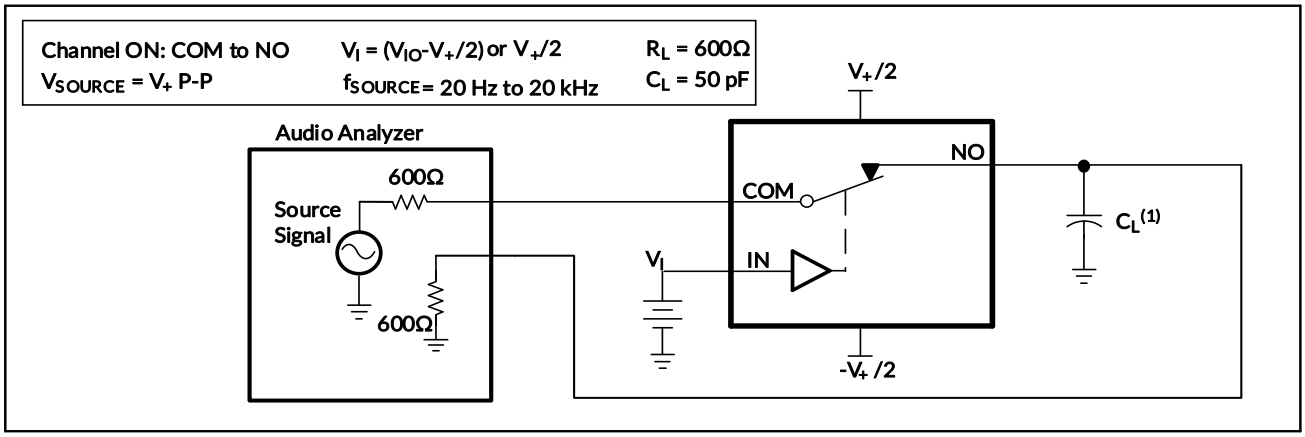
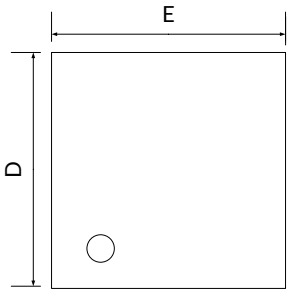


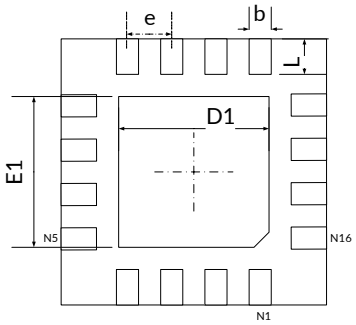
Figure 14. Total Harmonic Distortion (THD)

10 PACKAGE OUTLINE DIMENSIONS

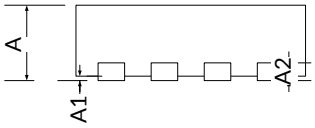
QFN3X3-16 (2)



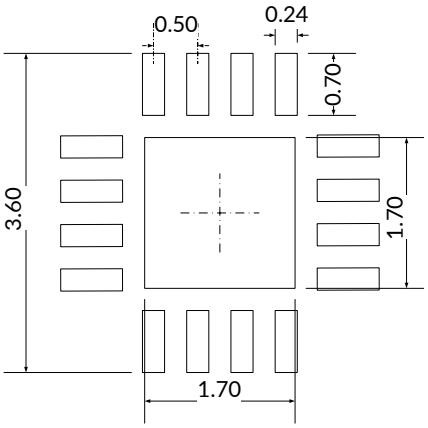
TOP VIEW



BOTTOM VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A (1)	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203		0.008	
b	0.180	0.300	0.007	0.012
D (1)	2.900	3.100	0.114	0.122
D1	1.600	1.800	0.063	0.071
E (1)	2.900	3.100	0.114	0.122
E1	1.600	1.800	0.063	0.071
e	0.500 TYP		0.020 TYP	
L	0.300	0.500	0.012	0.020

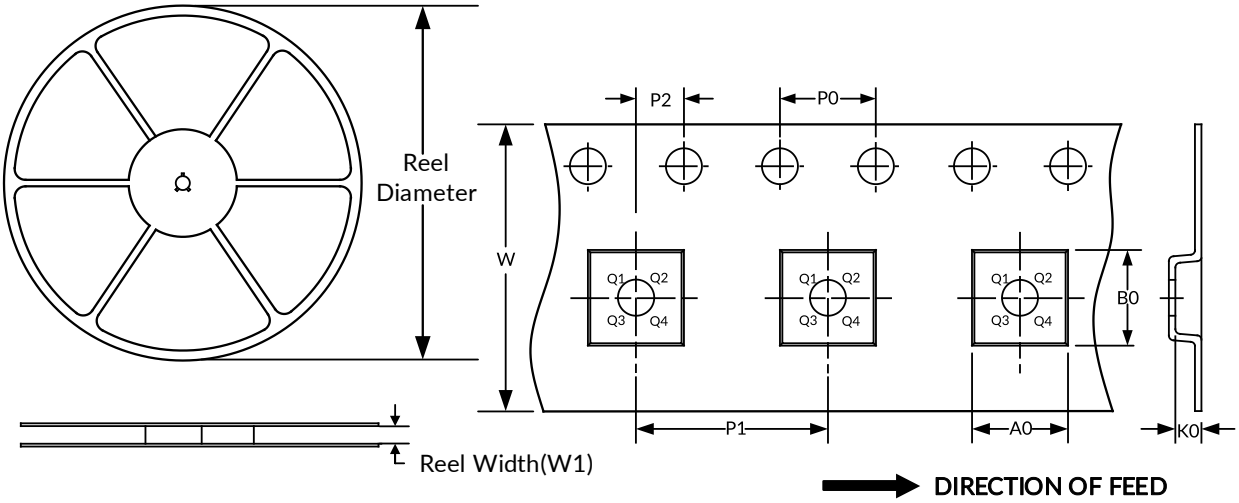
NOTE:

1. Plastic or metal protrusions of 0.075mm maximum per side are not included.
2. This drawing is subject to change without notice.

11 TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
QFN3X3-16	13"	12.4	3.35	3.35	1.13	4.0	8.0	2.0	12.0	Q1

NOTE:
1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

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